

राष्ट्रीय प्रौद्योगिकी संस्थान मिजोरम

NATIONAL INSTITUTE OF TECHNOLOGY MIZORAM

(An Institute of National Importance under Ministry of Education, Govt. of India)

CHALTLANG, AIZAWL, MIZORAM – 796012



**Course Structure & Syllabus for B.Tech Programme in
Electronics and Communication Engineering (ECE)
BATCH: 2019-20 onwards**

सूक्ष्म कणिका एवं संचार अभियांत्रिकी विभाग

Department of Electronics & Communication Engineering

Course Structure & Syllabus for B.Tech Programme in Electronics and Communication Engineering (ECE)

Classification of Credits Points:

1 Hr Lecture (L) per week	1.0 Credit
1 Hr Tutorial (T) per week	1.0 Credit
1 Hr Laboratory (P) per week	0.5 Credit
AUDIT Course	NO Credit

SEMESTER I				
Course Code	Course Name	Category	L-T-P	Credit
HUL 1101	Communicative English	DC	2-0-0	2
MAL 1101	Engineering Mathematics I	DC	3-1-0	4
CHL 1101	Engineering Chemistry	DC	3-0-0	3
EEL 1101	Basic Electrical Engineering	DC	3-0-0	3
MEL 1101	Engineering Mechanics	DC	3-0-0	3
EEP 1101	Basic Electrical Engineering Laboratory	DC	0-0-3	1.5
CHP 1101	Engineering Chemistry Laboratory	DC	0-0-3	1.5
MEP 1101	Engineering Mechanics Laboratory	DC	0-0-3	1.5
HUP 1101	Language Laboratory	DC	0-0-2	1
OBE 1101	Outcome Based Education	DC	1-0-0	AUDIT
TOTAL			15-1-11	20.5

SEMESTER II				
Course Code	Course Name	Category	L-T-P	Credit
ECL 1201	Basic Electronics Engineering	DC	3-0-0	3
HUL 1202	Social Science	DC	2-0-0	2
MAL 1202	Engineering Mathematics II	DC	3-1-0	4
PHL 1201	Engineering Physics	DC	3-0-0	3
CSL 1201	Introduction to Computer Programming	DC	3-0-0	3
MEP 1201	Engineering Drawing	DC	0-0-4	2
CSP 1201	Introduction to Computer Programming Laboratory	DC	0-0-3	1.5
PHP 1201	Physics Laboratory	DC	0-0-3	1.5
MEP 1202	Workshop	DC	0-0-3	1.5
ECA 1201	Extracurricular Activity	DC	0-0-0	AUDIT
TOTAL			14-1-13	21.5

SEMESTER III				
Course Code	Course Name	Category	L-T-P	Credit
ECL 1301	Solid State Devices	DC	3-0-0	3
ECL 1302	Digital Logic Design	DC	3-0-0	3
ECL 1303	Signals and Networks	DC	3-1-0	4
EEL 1308	Electrical and Electronics Measurements	DC	3-1-0	4
HUL 1301	Managerial Economics	DC	3-0-0	3
ECP 1302	Digital Logic Design Laboratory	DC	0-0-3	1.5
EEP 1308	Electrical and Electronics Measurement Laboratory	DC	0-0-2	1
TOTAL			15-2-5	19.5

SEMESTER IV				
Course Code	Course Name	Category	L-T-P	Credit
ECL 1401	Analog Circuits	DC	3-1-0	4
ECL 1402	Analog Communication	DC	3-1-0	4
ECL 1403	Linear Integrated Circuits	DC	3-0-0	3
CSL 1401	Computer Organization and Architecture	DC	3-0-0	3
MAL 1404	Probability Theory and Stochastic Processes	DC	3-0-0	3
ECP 1401	Analog Circuits Laboratory	DC	0-0-3	1.5
ECP 1402	Analog Communication Laboratory	DC	0-0-3	1.5
ECP 1403	Linear Integrated Circuits Laboratory	DC	0-0-3	1.5
TOTAL			15-2-9	21.5

SEMESTER V				
Course Code	Course Name	Category	L-T-P	Credit
ECL 1501	Digital Signal Processing	DC	3-1-0	4
ECL 1502	Microprocessors and Microcontrollers	DC	3-0-0	3
ECL 1503	Introduction to VLSI Design	DC	3-0-0	3
ECL 1504	Electromagnetic Theory	DC	3-0-0	3
EEL 1507	Control Systems – I	DC	3-0-0	3
ECP 1501	Digital Signal Processing Laboratory	DC	0-0-3	1.5
ECP 1502	Microprocessors and Microcontrollers Laboratory	DC	0-0-3	1.5
TOTAL			15-1-6	19

SEMESTER VI				
Course Code	Course Name	Category	L-T-P	Credit
ECL 1601	Digital Communication	DC	3-1-0	4
ECL 1602	RF & Microwave Engineering	DC	3-1-0	4
ECL 1603	Computer Communication Networks	DC	3-0-0	3
CSL/ECL 16XX	Program Elective I	DE	3-0-0	3
ECL 1604	Optoelectronic Devices and Circuits	DC	2-0-0	2
ECP 1601	Digital Communication Laboratory	DC	0-0-3	1.5
ECP 1602	RF & Microwave Engineering Laboratory	DC	0-0-3	1.5
ECD 1601	Industrial Training and Seminar	DC	0-0-2	1
TOTAL			14-2-8	20

SEMESTER VII				
Course Code	Course Name	Category	L-T-P	Credit
ECL 1701	Optical Communication and Network	DC	3-0-0	3
ECL 1702	Wireless Communication	DC	3-0-0	3
ECL 17XX	Program Elective II	DE	3-0-0	3
ECL 17XX	Program Elective III	DE	3-0-0	3
ECD 1701	Project Phase – I	DC	0-0-8	4
ECD 1702	Seminar	DC	0-0-2	1
TOTAL			12-0-10	17

SEMESTER VIII				
Course Code	Course Name	Category	L-T-P	Credit
ECL 18XX	Program Elective IV	DE	3-0-0	3
CHL 1802	Environmental Science	DC	2-0-0	AUDIT
CSL/ECL/EEL 1XXX	Open Elective	DE	3-0-0	3
ECP 1801	VHDL Design Laboratory	DC	0-0-2	1
ECD 1801	Project Phase – II	DC	0-0-18	9
ECD 1802	Grand Viva	DC	0-0-2	1
TOTAL			8-0-22	17

SEMESTER WISE CREDIT POINT(s)

SEMESTER	I	II	III	IV	V	VI	VII	VIII
CREDIT POINT	20.5	21.5	19.5	21.5	19	20	17	17
TOTAL								156

Structure of UG programme (B.Tech in ECE) and corresponding credit point(s) in the Curriculum

Sl. NO.	TOPIC / RELEVANT AREA	CREDIT POINT
1	Humanities and Social Science including Management	8
2	Basic Sciences	20
3	Engineering Sciences including Workshop, Drawing, Basics of Electrical/ Mechanical/ Computer etc.	20
4	Professional Core Subjects	77
5	Professional Subjects: Subjects relevant to chosen specialization/branch	12
6	Open Subjects: Electives from other technical and/or emerging subjects	3
7	Project Work, Seminar and Internship in Industry or Elsewhere	16
8	Mandatory Courses (Environmental Science, Induction Program, Indian Constitution, Essence of Indian Traditional Knowledge)	AUDIT
TOTAL		156

ELECTIVES**Program Elective(s) [PE I]**

Sl. No.	Course Code	Course Name
1	ECL 1XXX	Antenna Engineering
2	ECL 1XXX	Low power VLSI Design
3	CSL 1XXX	Software Engineering

Program Elective(s) [PE II – PE IV]

Sl. No.	Course Code	Course Name
1	ECL 1XXX	Foundations of MEMS
2	ECL 1XXX	Antenna Engineering
3	ECL 1XXX	Information Theory and Coding
4	ECL 1XXX	Sensors and Instrumentation
5	ECL 1XXX	Low power VLSI Design
6	ECL 1XXX	Nano Electronics
7	ECL 1XXX	Radar Communication
8	ECL 1XXX	CAD for VLSI
9	ECL 1XXX	Digital Image Processing
10	ECL 1XXX	Satellite Communication
11	ECL 1XXX	Wireless Sensor Networks
12	ECL 1XXX	Numerical Techniques in Electromagnetics
13	ECL 1XXX	Advanced Semiconductor Device Physics
14	ECL 1XXX	Semiconductor Process Technology
15	ECL 1XXX	VHDL Modelling
16	ECL 1XXX	Detection and Estimation Theory

Open Elective(s) [OE]

Sl. No.	Course Code	Course Name
1	ECL 1XXX	Nano Electronics
2	ECL 1XXX	Wireless Sensor Networks
3	ECL 1XXX	VHDL Modelling
4	ECL 1XXX	CAD for VLSI
5	ECL 1XXX	Information Theory and Coding

FIRST SEMESTER

HUL 1101	Communicative English
L-T-P: 2-0-0	Credits: 2
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

MAL 1101	Engineering Mathematics I
L-T-P: 3-1-0	Credits: 4
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

CHL 1101	Engineering Chemistry
L-T-P: 3-0-0	Credits: 3
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

EEL 1101	Basic Electrical Engineering
L-T-P: 3-0-0	Credits: 3
As per the syllabus prescribed by Dept. of Electrical and Electronics Engineering.	

MEL 1101	Engineering Mechanics
L-T-P: 3-0-0	Credits: 3
As per the syllabus prescribed by Dept. of Mechanical Engineering.	

EEL 1101	Basic Electrical Engineering Laboratory
L-T-P: 0-0-3	Credits: 1.5
As per the syllabus of EEL 1101	

CHP 1101	Engineering Chemistry Laboratory
L-T-P: 0-0-3	Credits: 1.5
As per the syllabus of CHL 1101.	

MEP 1101	Engineering Mechanics Laboratory
L-T-P: 0-0-3	Credits: 1.5
As per the syllabus of MEL 1101.	

HUP 1101	Language Laboratory
L-T-P: 0-0-2	Credits: 1
As per the syllabus of HUL 1101.	

OBE 1101	Outcome Based Education
L-T-P: 0-0-2	AUDIT
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

SECOND SEMESTER

ECL 1201
L-T-P: 3-0-0

Basic Electronics Engineering
Course Outcome

Credits: 3

- CO1 Identify semiconductors, metals and insulators
- CO2 Understand essential operation of semiconductor diodes and transistors
- CO3 Characterize and analyze semiconductor diodes, transistors and opamps
- CO4 Design simple rectifier, oscillator and opamp circuits
- CO5 Understand the fundamentals of digital number system

Syllabus

Semiconductors and diodes:

Lectures: 10

Introduction, Insulators, semiconductors and metals, Intrinsic and extrinsic semiconductors, PV cells, PN junction diode - Characteristics and analysis, Avalanche and Zener breakdown (zener diode).

Diode applications:

Lectures: 06

Rectifiers and filter circuit: Half wave rectifier, Full wave rectifier, bridge rectifier and their analysis, passive filters (RC; low pass, high pass filter), Series and shunt diode clippers, clipping at two independent levels, clamping operation, clamping circuit, Basic regulator supply using Zener diode.

Other diodes:

Lectures: 03

Photodiode, LED, Varactor, tunnel diode and PV Cell.

Transistors:

Lectures: 06

Construction and characteristics of BJT, Transistor configuration: CB, CE, CC configuration and their input output characteristics.

Field effect transistor:

Lectures: 06

Construction of JFET, pinch-off voltage, volt-ampere characteristics, transfer characteristics, types of MOSFET (enhancement and depletion) construction and characteristics

Amplifiers and oscillators:

Lectures: 04

Classification of amplifiers, concept of feedback, Characteristics of feedback amplifiers, basics of oscillator, barkhausen criterion, introduction to Op-Amp.

Basic Digital Logic:

Lectures: 03

Boolean Algebra, Basic Logic Gates, Number System

Text Books:

1. Integrated Devices & Circuits by Millman & Halkias.
2. Electronics Devices and Circuit Theory by R. Boylestad.
3. Electronics: Fundamental and Applications, 15th eds by D. Chattopadhyay and P C Rakshit.
4. Digital Circuits Vol. I (Combinational Circuits) by Diptiman Ray Chauduri

Reference Books:

1. Electronics Devices and Circuits-II by A.P.Godre & U.A. Bakshi.
2. Electronics Devices and Circuit by G.K. Mithal.
3. Microelectronic Circuits by Sedra Smith, Oxford University Press.

HUL 1202	Social Science
L-T-P: 2-0-0	Credits: 2
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

MAL 1202	Engineering Mathematics II
L-T-P: 3-1-0	Credits: 4
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

PHL 1201	Engineering Physics
L-T-P: 3-1-0	Credits: 4
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

CSL 1201	Introduction to Computer Programming
L-T-P: 3-0-0	Credits: 3
As per the syllabus prescribed by Dept. of Computer Science Engineering.	

MEP 1201	Engineering Drawing
L-T-P: 0-0-4	Credits: 2
As per the syllabus prescribed by Dept. of Mechanical Engineering.	

CSP 1201	Introduction to Computer Programming Laboratory
L-T-P: 0-0-3	Credits: 1.5
As per the syllabus of CSL 1201.	

PHP 1201	Physics Laboratory
L-T-P: 0-0-2	Credits: 1
As per the syllabus of PHL 1201.	

MEP 1202	Workshop
L-T-P: 0-0-3	Credits: 1.5
As per the syllabus prescribed by Dept. of Mechanical Engineering.	

ECA 1201	Extracurricular Activity
L-T-P: 0-0-0	AUDIT

THIRD SEMESTER

ECL 1301

Solid State Devices

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 To understand the physics and properties of Semiconductor
CO2 To understand the construction and working of p-n junction
CO3 To understand the construction and working of Bipolar Junction Transistor
CO4 To understand the fundamentals of Metal Semiconductor Contacts
CO5 To understand the construction and working of Metal Oxide Semiconductor System

Syllabus

Physics and Properties of Semiconductors:

Lectures: 08

Crystal structure, energy bands, statistics, Fermi level, carrier concentration at thermal equilibrium, carrier transport phenomena, Hall effect, recombination, optical and thermal properties, continuity equation, basic properties for semiconductor operation.

P-N Junction:

Lectures: 08

Physical Description of p-n junction, Poisson's Equation, current flow at a junction, I-V characteristics, Quantitative analysis of p-n diode characteristics, equivalent circuit, temperature dependence, Capacitance of p-n junction diode (transition & storage), junction breakdown (Avalanche & Zener), heterojunction.

Bipolar Junction Transistor:

Lectures: 06

Transistor action and dependence on device structure, Ebers-Moll Model, Coupled-Diode model current-voltage characteristics, non-ideal and limiting effects at extremes of bias.

Metal-Semiconductor Contacts:

Lectures: 04

Equilibrium, idealized metal semiconductor junctions, non-rectifying (Ohmic) contacts, Schottky diodes, tunneling

Metal-Oxide-Semiconductor System:

Lectures: 12

MOS structure, MOS capacitance (Operation with band diagram, threshold voltage, flatband voltage, CV characteristics, diffusion capacitance), Basic operation of Enhancement & Depletion mode MOSFET (saturation mode, transfer characteristics, mobility, electric field, velocity saturation), PMOS, NMOS, CMOS inverter

Text Books:

1. Solid State Electronic Devices, Ben G. Streetman & Sanjay Banerjee, 6th Edition, Pearson, 2005.
2. Semiconductor Devices- Physics and Tech., Nandita Dasgupta & Amitava DasGupta, PHI, 2010.

Reference Books:

1. Semiconductor physics and Devices by Donald A. Neamen, 4th Edition, TMH, 2012.
2. Microelectronics by Jacob Millman & Arvin Grabel, 2nd Edition, TMH, 2004.
3. Semiconductor Optoelectronic Devices by Pallab Bhattacharaya, 2nd Edition, PHI, 2004.

ECL 1302

Digital Logic Design

L-T-P: 3-0-0

Course Outcome

Credits: 3

CO1 Design and analyze combinational and sequential logic circuits through HDL models

CO2 Optimize combinational and sequential logic circuits

CO3 Understand fault detection techniques for digital logic circuits

CO4 Analyze a memory cell and apply for organizing larger memories

Syllabus

Introduction to Boolean Algebra and Logic Gates:

Lectures: 08

Signed binary number, Binary arithmetic, Codes – BCD, Gray, Excess-3, Error detection & Correcting code-Hamming code, Logic Gates, Universal gates, Boolean Algebra, Basic theorems & properties of Boolean Algebra, De-Morgan's theorem, Min terms & Max terms, K-map representation, Q-M Method, simplification and realization with logic gates

Combinational Circuits:

Lectures: 06

Code Converters, Adders (Half and Full adders, parallel binary adders, look ahead carry adder generator, BCD Adder), Subtractor (Half and Full subtractor), decoders and Encoders, Priority Encoder, Multiplexer and De-multiplexer, Parity generator/checkers.

Sequential Logic:

Lectures: 08

Latches, Flip-Flops (SR, D, JK, T and Master Slave JK, Edge Triggered), Conversion of Flip-Flops, Glitches, Shift Register (SISO, SIPO, PIPO, PISO, Bidirectional), Counter (ripple and synchronous, Ring and Johnson Counters).

Memory:

Lectures: 04

Memory concepts, RAM, ROM, UV-EPROM, EEROM, Flash memory, Optical memory.

Programmable Logic Devices:

Lectures: 02

PAL, PLA, PROM, CPLD, FPGA and Programmable ASIC.

Finite State Machine (FSM):

Lectures: 04

Brief introduction to finite automata theory; Moore, Mealy and Turing machine; state diagram, state variable, state table and state minimization. Design of state machines using combinational logic circuits and memories.

Introduction to Logic Families:

Lectures: 04

Standard logic families (TTL, ECL, CMOS).

D/A and A/D:

Lectures: 04

Sample and Hold Circuits, Digital to Analog converter (Binary weighted resistor network & R-2R ladder network), Analog to Digital converter (Flash type, Counter type, Dual Slope & Successive approximation type).

Text Books:

1. Digital Logic and Computer Design, M. Morris Mano, PHI, 2008.
2. Digital design- Principles and Practices, J. F. Wakerly, 4th Edition, Pearson, 2006.
3. Digital Integrated Electronics, Herbert Taub (Author), Donald Schilling (Author)
4. Digital Principles and Applications, Leach, Malvino, Saha

Reference Books:

1. Digital Fundamentals, Thomas L.Floyd, 10th Edition, Pearson, 2011.
2. Digital Principles and Applications, Donald P. Leach, Albert Paul Malvino, 5th ed, TMH, 1995.
3. Switching & Finite Automata Theory, Zvi Kohavi, 2nd Edition, TMH.,2008.
4. Fundamentals of Digital Logic, Anand Kumar, 2nd Edition, PHI, 2008.
5. Fundamentals of Logic Design, Charles H. Roth Jr, 4th Ed, Jaico publishers, 2002.

ECL 1303

Signals and Networks

L-T-P: 3-1-0

Course Outcome

Credits: 4

- CO1 Solve network problems using mesh current and node voltage equations
- CO2 Design resonant circuits for given bandwidth
- CO3 Compute responses of first order and second order networks using time domain analysis
- CO4 Obtain circuit response using Laplace Transform
- CO5 Analyze networks using Thevenin, Norton, Maximum power transfer, Superposition, Miller and Tellegen's theorems

Syllabus

Signals and Systems:

Lectures: 11

Classification of signals, signal operations, signal properties, system properties: linearity, time/shift-invariance, causality, stability; continuous-time linear time invariant (LTI) and discrete-time linear shift invariant (LSI) systems: impulse response and step response; response to an arbitrary input: convolution; system representation using differential and difference equations; Eigen functions of LTI/ LSI systems, frequency response and its relation to the impulse response, Hilbert transform, random and ergodic processes, sampling theory, reconstruction of signal.

Network Theorems:

Lectures: 05

Superposition theorem, maximum power transfer theorem, reciprocity theorem, Millman's theorem, substitution theorem, compensation theorem, Tellegen's theorem, and all theorems using examples of AC networks.

Transient Analysis and Resonance:

Lectures: 06

Introduction of transient phenomena, initial conditions and analysis of RL, RC and RLC circuits; series resonance, parallel resonance and comparison of series and parallel resonant circuits.

Two Port Network:

Lectures: 08

One port and two port network, Sign convention, Admittance Parameter, Parallel connection of two port network, Impedance parameter, Series connection of two-port network. Hybrid parameters, Inverse Hybrid parameters, Transmission parameters, Inverse Transmission parameters, Concept of driving point impedance and admittance, Symmetrical two ports and bisection, Image impedance.

Graph Theory:

Lectures: 03

Graph of a network, Trees, Co-trees, Loops, Incidence matrix, cut-set matrix, Ties matrix and loop currents, Number of possible trees of a Graph, Analysis of Networks

Application of Laplace Transform:

Lectures: 06

Brief review of Laplace transform technique, Initial and final value Theorem, Solution of circuit transient using Laplace transform. Use of Laplace's transform in electrical circuit analysis.

Fourier analysis:

Lectures: 04

Trigonometric Fourier Series, Evaluation of Fourier Coefficients, Waveform Symmetry, Exponential form, Fourier transform techniques applied in networks.

Filter Circuits:

Lectures: 06

Classification of filters, equation of an ideal filter, Theory of pie section, Constant K-type filters, low pass filters, design of low pass filter, high pass filters, band pass filters, band rejection filters and all pass filters. M derived filters, theory of M-derived filters, M-derives low pass and high pass filters. Approximation theory of filters (Butter worth and Chebyshev). Impedance matching.

Text Books:

1. Hayt & Kemmerly, Engineering Circuit Analysis, Mc Graw Hill.
2. Roy Choudhury, Network and Systems, New Age.
3. Signals and System by Oppenheimer.

Reference Books:

1. Rajeswaran, Electric circuit Theory, Pearson publications.
2. Wadhwa, Network analysis and synthesis, New Age Publication
3. Soni and Gupta, A Course in Electrical Circuit Analysis, Dhanpat Rai & Sons
4. Van Valkenburg, Network Analysis & Synthesis, PHI publications

EEL 1308	Electrical and Electronics Measurements
L-T-P: 3-1-0	Credits: 4
As per the syllabus prescribed by Dept. of Electrical and Electronics Engineering.	

HUL 1301	Managerial Economics
L-T-P: 3-0-0	Credits: 3
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

EEP 1308	Electrical and Electronics Measurement Laboratory
L-T-P: 0-0-2	Credits: 1
As per the syllabus of EEL 1302.	

ECP 1302

Digital Logic Design Laboratory

L-T-P: 0-0-3

Credits: 1.5

Experiment List:

1. Verification of truth-table of different logic gates: To Study and verify Truth Table of different logic gates.
2. Implementation of various logic gates using universal gates: To Implement various logic gates using universal gates.
3. Implementation of XOR & XNOR using universal gates: To Implement XOR & XNOR using universal gates.
4. Implementation of half adder & full adder using universal gates: To Implement half adder & full adder using universal gates.
5. Implementation of half subtractor & full subtractor using universal gates: To Implement half subtractor & full subtractor using universal gates.
6. Implementation of 8x1 MUX using 4x1 MUX and to realize the given Boolean expression: To Implement 8x1 MUX using 4x1 MUX and to realize the given Boolean expression.
7. Design of 4-bit binary-to-gray code converter: To Design a 4-bit binary-to-gray code converter.
8. Design of 4-bit gray-to-binary code converter: To Design a 4-bit gray-to-binary code converter.
9. Design of 4-bit BCD-to-excess-3 code converter: To Design a 4-bit BCD-to-excess-3 code converter.
10. Implementation of half adder & half subtractor using 2-line to 4-line decoder: To Implement a half adder & half subtractor using 2-line to 4-line decoder.
11. Design of T-flip flop using SR flip flop: To Design a T-flip flop using SR flip flop.
12. Design of a clocked flip flop using 3 input NAND gate and verify the truth table: To Design a clocked flip flop using 3 input NAND gate and verify the truth table.
13. Design a 3-bit binary counter using J-K flip flop: To Design a 3-bit binary counter using JK-flip flop.
14. Design of a Ring Counter & Johnson Counter: To Design a Ring Counter & Johnson Counter.
15. Design of Asynchronous counter & Synchronous Counter: To Design an Asynchronous counter & Synchronous Counter.
16. Realization of Shift Registers: To Design and Realize a given Shift Register.
17. Realization of Encoders: To Design and Realize a given Encoder.

FOURTH SEMESTER

ECL 1401

Analog Circuits

L-T-P: 3-1-0

Course Outcome

Credits: 4

- CO1 Design and analyze multistage amplifiers.
- CO2 Apply compensation techniques for stabilizing analog circuits against parameter variations
- CO3 Design negative feedback amplifier circuits and oscillators
- CO4 Analyze and design solid state power amplifier circuits.
- CO5 Analyze and design tuned amplifier circuits.

Syllabus

Bipolar Junction Transistors (BJTs):

Lectures: 12

Characteristics of BJT; Ebers-Moll equations and large signal models; inverse mode of operation, early effect; BJT as an amplifier and as a switch; DC biasing of BJT amplifier circuits; small signal operations and models; Single state BJT amplifiers – CE, CB and CC amplifiers; high frequency models and frequency response of BJT amplifiers; Basic design in discrete BJT amplifiers; complete design examples; Basic BJT digital logic inverter.

Metal Oxide Semiconductor Field Effect Transistors (MOSFETs):

Lectures: 12

MOSFET operational Characteristics; PMOS, NMOS and CMOS current voltage characteristics; DC analysis; Constant Current Sources and Sinks; MOSFET as an Amplifier and as a Switch; Biasing on MOS Amplifiers; Small Signal Operation of MOS amplifiers; Common-source, common gate and source Follower Amplifiers; CMOS amplifiers; MOSFET Digital logic inverters; voltage transfer characteristics.

Amplifier Classes:

Lectures: 06

Classification of amplifiers; Class A, Class B, Class AB Class C – Circuit operation, transfer characteristics, power dissipation, efficiency. Practical BJT and MOS power transistors; thermal resistance; heat sink design.

Feedback Amplifiers and Oscillators:

Lectures: 10

Basic feedback topologies; Analysis of Series-shunt, series-series, shunt-shunt and shunt-series feedback amplifiers; stability in feedback amplifiers, frequency compensation; principle of sinusoidal oscillators and Barkhausen criterion; Active-RC and Active-LC sinusoidal oscillators; Wien Bridge; Phase-Shift; Quadrature Oscillators; Crystal Oscillators, application in voltage regulation.

Text Books:

1. Microelectronic Circuits, Adel S. Sedra and Kenneth Carless Smith, 5th Edition, Oxford, 2004.
2. Analysis and Design of Analog Integrated Circuits, Gray and Meyer, 5th Edition, 2009.
3. Millman's Integrated Electronics: Analog and Digital Circuits and Systems, 2nd eds by Jacob Millman, Christos Halkias and Chetan D Parikh.
4. Electronic Principles Book by Albert Paul Malvino, 1998

Reference Books:

1. Electronic devices and Circuit Theory, Robert Boylestad, 9th Edition, Pearson Education, 2007.
2. Microelectronics: Analysis and Design, Sundaram Natarajan, TMH, 2006.
3. Electronic Circuits, D.L. Schilling and C. Belove, TMH.
4. The Art of Electronics, Paul Horowitz and Winfield Hill

ECL 1402

Analog Communication

L-T-P: 3-1-0

Course Outcome

Credits: 4

- CO1 Compare the performance of AM, FM and PM schemes with reference to SNR
- CO2 Understand noise as a random process and its effect on communication receivers
- CO3 Evaluate the performance of PCM, DPCM and DM in a digital communication system
- CO4 Identify source coding and channel coding schemes for a given communication link

Syllabus

Introduction to Noise:

Lectures: 06

Thermal noise, shot noise, White noise, Narrow band noise and its representation, signal to noise Ratio (SNR), noise temperature, noise equivalent bandwidth, noise figure, spectral analysis of noise.

Analog Modulation Techniques:

Lectures: 08

Introduction, amplitude modulation (AM); different type of AM, AM power calculations, analog modulation: frequency modulation (FM) and phase modulation (PM); spectra of FM signals, narrow band and wide band FM, transmission bandwidth of FM, frequency translation and multiplexing.

AM Generation and Reception:

Lectures: 12

Introduction, generation of AM signals, square law modulator, Vander Bijl modulator, suppressed carrier AM generation, ring modulator, balanced modulator. Tuned radio frequency (TRF) receiver, basic elements of AM super-heterodyne receiver; RF amplifiers characteristics-sensitivity, dynamic range, gain compression, selectivity, image frequency rejection, mixers, tracking and alignment, local oscillator, IF amplifier, AM detectors; envelope detection, AGC. Generation of SSB. vestigial side-band modulation (VSB). Detection of SSB and VSB signals. Noise in AM receivers using envelop detection and coherent detection, figure of merits.

FM Generation and Reception:

Lectures: 08

Generation of FM by direct methods. Indirect generation of FM: the Armstrong method. FM receiver direct methods of frequency demodulation; slope detector, Foster Seely or phase discriminator, ratio detector, indirect methods of FM Demodulation: FM detector using PLL. Noise in FM receivers, pre-emphasis and de-emphasis.

Pulse Modulation Transmission and Reception:

Lectures: 04

Introduction, pulse amplitude modulation (PAM), PAM modulator circuit, demodulation of PAM signals, pulse time modulation (PTM): pulse width modulation (PWM), pulse position modulation (PPM); PPM demodulator.

Text Books:

1. Modern Digital and Analog Communication Systems, B P Lathi, 4th Edition, OUP, 2009.
2. Principle of Communication Systems, Herbert Taub & Donald L. Schilling, TMH.
3. Communication Systems, Simon Haykin, John Wiley and Sons, 5th Edition, 2009.

Reference Books:

1. Electronic Communication, Dennis Roddy, John Coolen, 4th Edition, Pearson, 1997.
2. Communication Systems, A. B. Carlson, 5th Edition, TMH/MGH.
3. Principles of communication Engineering, Umesh Sinha.
4. Communication Theory, T. G. Thomas & S Chandrasekhar, TMH, 2005.
5. Communication system engineering, J.G. Proakis and Salehi, 2nd Edition, PHI, 2001.
6. Digital and Analog Communication Systems, Leon W. Couch, 7th Edition, Pearson, 2008.
7. Contemporary Communication Systems using MATLAB and Simulink, J. G. Proakis & Masoud Salehi & Gerhard Bauch, 3rd Edition, Cengage Learning, 2013.

ECL 1403

Linear Integrated Circuits

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Design op-amp circuits to perform arithmetic operations.
- CO2 Analyze and design linear and non-linear applications using op-amps.
- CO3 Analyze and design oscillators and filters using functional ICs.
- CO4 Choose appropriate A/D and D/A converters for signal processing applications

Syllabus

Differential Amplifiers:

Lectures: 10

Advantages of differential amplifiers; MOS and BJT differential pair; Small signal and large signal operation of differential pairs; Parameters and non-ideal characteristics of differential amplifiers; differential amplifier with active load frequency response, constant current bias, current mirror, cascaded differential amplifier stages, level translator.

Operational Amplifiers and its Applications:

Lectures: 10

Concept of operational amplifiers; Ideal operational amplifier parameters; Inverting and non-inverting configurations; Common OP AMP Ics: Input offset voltage, input bias and offset current, Thermal drift, CMRR, PSRR, Frequency response. Gain-frequency and Slew rate; DC, ac amplifiers, summing differential amplifier, V to I and I to V converters, Instrumentation amplifiers; Integrators, Differentiators; Logarithmic Amp; Multipliers; Comparators; Schmitt triggers. Limiters, log/antilog amplifiers, multipliers, function generators, waveform generators.

Filters and Tuned Amplifiers:

Lectures: 10

Filter characteristics and specifications; First and Second Order Filter functions; First-order and second order filter network using OPAMPS (low/high/pass band pass/band reject/ All pass filter); Tuned Amplifiers; Basic principle; amplifiers with multiple tuned circuits; Synchronous and Stagger tuning; RF amplifiers considerations.

Waveform Generation and Shaping Circuits:

Lectures: 10

Multivibrators – Astable, monostable and bistable circuits; bistable circuit as memory element comparator generation of square, triangular waveforms and standardized pulse using AMV and MMV; Application of 555 timer.

Text Books:

1. Analysis and Design of Analog Integrated Circuits, Gray and Meyer, 5th Edition, John Wiley & Sons, Incorporated, 2010.
2. RF Microelectronics, Behzad Razavi, 2nd Edition, Pearson Education International, 2012.
3. CMOS Analog Circuit Design, Philips E. Allen & Douglas, R. Holberg, Elsevier Publisher, 2011.
4. Op Amp and Linear ICs, R. A. Gackward, 4th Edition, PHI/Pearson Education, 2002.

Reference Books:

1. Electronic devices and Circuit Theory, Robert L. Boylestad, Louis Nashelsky, Pearson/Prentice Hall, 2009.
2. Electronic Circuits, D.L. Schilling and C. Belove, TMH.

CSL 1401 Computer Organization and Architecture	
L-T-P: 3-0-0	Credits: 3
As per the syllabus prescribed by Dept. of Computer Science Engineering.	

MAL 1404 Probability Theory and Stochastic Processes	
L-T-P: 3-0-0	Credits: 3
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

ECP 1401**Analog Circuits Laboratory****L-T-P: 0-0-3****Credits: 1.5****Experiment List:**

1. To identify the various Electronics Instruments and Electronic circuit components.
2. To study the characteristics of Zener diode under forward and reverse bias condition.
3. To study the operation of half-wave Rectifier.
4. To study the operation of Full-wave rectifier.
5. To operation of bridge Rectifier.
6. To plot the transistor characteristic of common-emitter configuration and to find the h-parameter for the same.
7. To plot the transistor characteristic of common-base configuration and to find the h-parameter for the same.
8. To plot the transistor characteristic of common-collector configuration and to find the h-parameter for the same.
9. To observe the clipping waveform in different clipping configurations.
10. To observe the clamping circuits, positive clamping circuit and negative clamping circuit.
11. Study the switching characteristics of transistor: Rise time, Fall time, ON/OFF time & Delay time.
12. To design and implement the fixed-bias amplifier circuit and obtain the waveforms at input and output terminals, the bias resistances, measure the gain & the frequency plot.
13. To design and implement the RC couple amplifier circuit and to find Cut off frequencies, Band width & Mid band gain input/output impedance.
14. To study the following application of op-amp using (741): Voltage follower, inverting amplifier, non-inverting amplifier, variable voltage gain amplifier, Adder & subtractor.
15. To design and construct a RC phase shift Oscillator for a given frequency, f_0 .
16. To design and construct a Wien-Bridge oscillator for a given Cut-off frequency.
17. To determine the parameters of the single-stage JFET amplifier (Common-Drain amplifier).
18. To design and test a Darlington current amplifier and find the following parameters:
 - i. Current Gain
 - ii. Voltage Gain
 - iii. Bandwidth
 - iv. Input and output impedance
19. To design and construct a class-A power amplifier and to determine its efficiency.
20. To Design and test the following Circuit Using 555
 - i. Astable multivibrator
 - ii. Monostable multivibrator
 - iii. Voltage to frequency converter (voltage-controlled oscillator)
 - iv. Schmitt trigger.
21. To design and study the applications 555 timer IC by operating in the monostable mode
 - i. Ramp generator
 - ii. Pulse width generator
 - iii. Frequency divide.

ECP 1402**Analog Communication Laboratory****L-T-P: 0-0-3****Credits: 1.5****Experiment List:**

1. Double Sideband Amplitude Modulation Transmission.
2. Double Sideband Amplitude Modulation Reception.
3. Calculation of modulation index of DSB wave by trapezoidal pattern.
4. Study of Diode Detector.
5. Single Sideband Amplitude Modulation Generation.
6. Single Sideband Amplitude Modulation Reception.
7. Operation of the Automatic Gain Control Circuit.
8. Frequency Modulation Technique.
9. Demodulation on FM signals: Ratio detector and Foster Seeley Detector.
10. Receiver Characteristics: Selectivity curve for radio receiver.
11. Sensitivity curve for radio receiver.
12. Fidelity curve for radio receiver.
13. To construct a triangular wave with the help of fundamental frequency.
14. To construct a rectangular sawtooth wave with the help of fundamental frequency and its harmonic component.
15. To construct a square wave with the help of fundamental frequency fundamental frequency and its harmonic component.

ECP 1403**Linear Integrated Circuits Laboratory****L-T-P: 0-0-3****Credits: 1.5****Experiment List:**

1. The Differential Amplifier.
2. The Op Amp Comparator.
3. The Non-inverting Voltage Amplifier.
4. The Inverting Voltage Amplifier.
5. The Op Amp Differential Amplifier.
6. Parallel-Series and Series-Series Negative Feedback.
7. Gain-Bandwidth Product.
8. Slew Rate and Power Bandwidth.
9. The Non-compensated Op Amp.
10. The Operational Transconductance Amplifier.
11. Precision Rectifiers.
12. The Triangle-Square Generator.
13. The Wien Bridge Oscillator.
14. The Integrator.
15. The Differentiator.

FIFTH SEMESTER

ECL 1501

Digital Signal Processing

L-T-P: 3-1-0

Course Outcome

Credits: 4

- CO1 Find DFT of a given signal through Fast Fourier Transform Techniques
- CO2 Design FIR and IIR type digital filters.
- CO3 Identify filter structures and evaluate the coefficient quantization effects
- CO4 Understand sample rate conversion techniques.
- CO5 Compare the architectures of DSP and General-Purpose Processors

Syllabus

Introduction:

Lectures: 06

Limitations of analog signal processing, Advantages of digital signal processing and its applications; Some elementary discrete time sequences and systems; Basic elements of digital signal processing such as convolution, correlation and autocorrelation, Concepts of stability, causality, linearity, and difference equations.

Z Transform:

Lectures: 05

Review and analysis of LTI system in Z-domain, definition and mapping between S-plane and Z-plane, unit circle, convergence and ROC, Z-Transform on sequences, Inverse Z-Transform and numericals.

Discrete Fourier Transform:

Lectures: 08

DFT and its properties; Linear Periodic and Circular convolution; Linear Filtering Methods based on DFT; Filtering of long data sequences; Fast Fourier Transform algorithm using decimation in time and decimation in frequency techniques; Linear filtering approaches to computation of DFT.

Digital filter structures:

Lectures: 03

System describing equations, filter categories, direct form I and II structures, cascade and parallel communication of second order systems, linear phase FIR filter structures, frequency sampling structure for the FIR filter.

IIR Filter design technologies:

Lectures: 04

Analog lowpass filter design techniques, methods to convert analog filters into digital filters, frequency transformations for converting lowpass filters into other types, all-pass filters for phase response compensation.

FIR filter design techniques:

Lectures: 03

Windowing method for designing FIR filters, DFT method for approximating the desired unit sample response, combining DFT and window method for designing FIR filters, frequency sampling method for designing FIR filters.

Finite precision effects:

Lectures: 02

Fixed point and Floating point representations, Effects of coefficient quantization, Effect of round off noise in digital filters, Limit cycles.

Application of DSP:

Lectures: 04

DTMF signal detection, residual sound Processing, Digital FM stereo generation, oversampling A/D, D/A converter.

Text Books:

1. Digital Signal Processing: Principles Algorithms and Applications, J. G. Proakis and D. G. Manolakis, Pearson Education, 4th Edition, 2007.
2. Digital Signal Processing, A. V. Oppenheim, R. W. Schaffer, Pearson Education, 2004.
3. Digital signal processing, S. Salivahanan, A Vallavaraj, C Gnanapriya, TMH, 2nd Edition, 2010.
4. Digital Signal Processing: Fundamentals and Application, Li Tan, Academic Press, Elseviers.
5. Digital Signal Processing, Ramesh Babu

Reference Books:

1. Digital Signal Processing: A computer-based approach, S. K. Mitra, 4th Edition, TMH, 2011.
2. Signal & Systems by Oppenheim A V, Willsky A S & Young I T, Prentice Hall.
3. Theory and Application of Digital Signal Processing, L. R. Rabiner and B. Gold, Pearson Education, 2004.
4. Digital Signal Processing, Emmanuel C. Ifeakor, Barrie W. Jervis, Pearson Education India, 2002.
5. Digital Filters, Analysis, Design and Applications, Andrias Antoniou, 2nd Edition, TMH, 2006.
6. Digital Signal Processing, D.J. DeFatta, J.G.Lucas and W. S. Hodgkiss, J Wiley and Sons.

ECL 1502 Microprocessors and Microcontrollers

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Understand the evolution of processor architectures
- CO2 Write simple programs in assembly language of Pentium processor
- CO3 Interface peripheral devices and memory with microcontrollers
- CO4 Program an ARM processor for DSP Applications

Syllabus

Introduction to Microprocessors & Microcontrollers:

Lectures: 02

General definitions of mini computers, microprocessors (8085 & 8086) and microcontrollers (8051, PIC).

80XX microprocessor:

Lectures: 14

CPU Architecture, Pin configuration, Instructions, Flag structure, Addressing Modes/ Instruction Word size, Languages, Description of Instructions, Assembly directive, Assembly software programs with algorithm will be new addition to the existing chapter and it will be extended to 8086. timing diagram, Instruction cycle, fetch cycle, execute cycle: I/O read cycle, I/O Write cycle, Memory Read, Memory Write, concept and structure of interrupts, interrupt service routines.

Methods of data transfer:

Lectures: 04

IN/OUT instructions with timing diagrams, programmable peripheral interfaces, display devices and DAC, ADC interfacing.

8051 Microcontroller:

Lectures: 10

Introduction, addressing modes and port structure, register bank, flags, program status word, memory map, external memory access, counters and timers, interrupts, instruction set.

PIC Microcontrollers:

Lectures: 10

Introduction, basic architecture, instruction set, input/output ports, timer modules, ADC module, Synchronous serial port module, I2C communication.

Text Books:

1. Microprocessor Architecture, Programming and Applications with 8085/ 8086 A, Ramesh S. Gaonkar, 5th Edition, PHI, 2002.
2. The 8051 Microcontroller and Embedded system, M.A. Mazidi, Rolin McKinlay, 2nd edition, Pearson, 2007.
3. PIC Microcontroller and Embedded Systems: Using assembly and C for PIC 18, Mazidi Muhammad Ali, 1st edition, Pearson Education India, 2008
4. Microprocessors & Interfacing, Douglas V. Hall, TMH, 2006.

Reference Books:

1. Advanced Microprocessor and Interfacing, Badri Ram, TMH, 2001.
2. The Intel Microprocessors Architecture, Programming & Interfacing, B. B. Brey, 8th Edition, Pearson and PHI, 2009.
3. Advanced Microprocessor & Peripherals, K.M. Bhurchandi & A.K. Ray, 3rd Edition, TMH, 2012.

ECL 1503

Introduction to VLSI Design

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Model the behaviour of a MOS Transistor
- CO2 Design combinational and sequential circuits using CMOS gates
- CO3 Identify the sources of power dissipation in a CMOS circuit.
- CO4 Analyze SRAM cell and memory arrays

Syllabus

Basics of fabrication and layout

Lectures: 03

Fabrication process flow (basic steps), stick diagrams & layout design.

Non-ideal effects in MOS:

Lectures: 03

Channel length modulation, substrate bias effect, DIBL, GIDL. Scaling and Moore's law.

MOS Inverter:

Lectures: 05

Ideal and practical inverter characteristic (Noise Margin, Propagation delay, Speed-Power product, Fan-in, Fan-out), Transfer Characteristics-MOS, CMOS inverter, Transient Analysis of CMOS Inverter and Delay analysis.

CMOS Logic Circuits:

Lectures: 04

NAND & NOR Gates, Complex Logic Circuits, Pseudo n-MOS logic, CMOS Full adder circuit, CMOS Transmission Gate (Pass transistor Logic).

Advanced CMOS Logic Circuits:

Lectures: 05

Dynamic CMOS Logic, Domino CMOS Logic, Differential Cascode voltage switch logic, NORA Logic.

Sequential CMOS Logic Circuits:

Lectures: 05

Behavior of Bi-stable elements, SR Latch Circuit, Clocked JK Latch/Master-Slave JK, CMOS D-latch and edge triggered Flip-flop.

Analysis of Single stage amplifier:

Lectures: 04

Frequency response and gain.

Physical Design:

Lectures: 08

Basics of partition, placement, floor planning and routing, diffusion length, lambda rule, metallic & dopant diffusion, mask design

Text Books:

1. CMOS Digital Integrated Circuits, Analysis & Design by Sung-Mo Kang & Yusuf Leblebici, 4th Edition, Tata McGraw-Hill Edition, 2013.
2. Digital Integrated Circuits by Rabaey & Chandrakashan, 2nd Edition, PHI, 2003.
3. CMOS VLSI Design: A Circuits and Systems Perspective by Neil H. E. Weste, Pearson Education India, 2005
4. Design of Analog CMOS Integrated Circuits by Behzad Razavi, McGraw-Hill Education, 2000.

Reference Books:

1. Analysis & Design of Digital Integrated Circuits by David Hodges, Horace G Jackson, & Resve A Saleh, Tata McGraw-Hill Edition, 1983.
2. Digital Integrated Circuits by Kenneth William Martin, Oxford University Press, 2000.
3. VLSI Design techniques for Analog and Digital Circuits, R. L. Geiger, P. E. Allen, Noel R. Strader, McGraw-Hill International Edition, 1990.

ECL 1504

Electromagnetic Theory

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Solve Maxwell's equations using vector calculus in three standard coordinate systems
- CO2 Deduce EM wave propagation in free space and in dielectric medium
- CO3 Analyze electromagnetic wave propagation in guiding structures under various matching conditions
- CO4 Understand the power flow mechanism in guiding structures and in unbounded medium

Syllabus

Review of co-ordinate systems and vector calculus:

Lectures: 03

Different coordinate systems, line, surface and volume integrals. Gradient, divergence and curl. Divergence theorem and Stokes theorem.

Review of Electrostatics:

Lectures: 05

Coulomb's law, Gauss's law and its applications. The potential functions, equipotential surface, Poisson's and Laplace's equation. Capacitance, electrostatics energy, conductor properties. Uniqueness theorem.

Review of Magneto Statics:

Lectures: 04

Biot-Savart law, Ampere circuital law, magnetic flux and magnetic flux density. Energy stored in magnetic field. Ampere's force law, Magnetic vector potential.

Maxwell's Equation:

Lectures: 05

Equation of continuity for time varying field, inconsistency of Ampere circuital law. Concept of displacement current, Maxwell's equation in integral and differential form. Maxwell equation for time harmonic fields, boundary condition

Electromagnetic waves:

Lectures: 04

Solution for free space condition, uniform plane waves, Wave propagation in lossless and lossy dielectric media, skin depth.

Reflections and Refractions of plane waves:

Lectures: 06

Reflection by a perfect conductor with normal and oblique incidence, reflection & refractions by perfect dielectrics with normal and oblique incidence. Surface impedance.

Poynting vector:

Lectures: 04

Poynting theorem, instantaneous average and complex Poynting vector, power loss in a plane conductor.

Wave propagation modes:

Lectures: 06

Ground wave propagation, sky wave, surface wave propagation

Text Books:

1. Element of Electromagnetics, Mathew N. O. Sadiku, 5th Edition, Oxford University, 2010.
2. Electromagnetic waves and Radiating Systems, E. C. Jordan & K. G. Balmain, 2nd Edition, PHI.
3. Electromagnetics Theory, David K Chang, 2nd edition, Addison Wesley Longman, 1999.
4. Engineering Electromagnetics, William Hayt, John Buck, 8th Edition, TMH, 2008.
5. Introduction to Electrodynamics, David Griffiths

Reference Books:

1. Introductory course in electromagnetic fields, P.V. Gupta, 2nd Edition, Dhanpetrai Pub., 1975.
2. Fundamental of Electromagnetics, M.A. Wazed Miah, 5th Edition, TMH, 1985.
3. Electromagnetic fields and waves, V. V. Sarvate, Bohem press, 1993.
4. Electromagnetics by B.B. Laud.
5. Electronic and Radio Engineering, Frederick Emmons Terman, 4th Edition, TMH, 1955.
6. Electromagnetic waves and radiating system, E.C Jordan & K.G Balmain, 2nd Edition, PHI, 1979.

EEL 1507	Control Systems – I	
L-T-P: 3-0-0		Credits: 3
As per the syllabus prescribed by Dept. of Electrical and Electronics Engineering.		

ECP 1501**Digital Signal Processing Laboratory****L-T-P: 0-0-3****Credits: 1.5****Experiment List:**

1. Introduction of Discrete signals.
2. Sampling of signals.
3. Cross correlation and auto correlation of two discrete sequences.
4. Convolution of two discrete sequences.
5. Discrete Fourier Transform of two discrete sequence and plot magnitude and phase spectrum.
6. Low Pass Filter and High Pass Filter (IIR) (a) Butterworth (b) Chebyshev.
7. Band Pass Filter and Band Stop Filter (IIR) (a) Butterworth (b) Chebyshev.
8. FIR Filter – Triangular Window.
9. FIR Filter – Hamming and Hanning Window.
10. FIR Filter – Blackman Window.

ECP 1502**Microprocessors and Microcontrollers Laboratory****L-T-P: 0-0-3****Credits: 1.5****Experiment List:**

-
1. Addition of Two 8-bit numbers.
 2. Subtraction of two 8-bit numbers
 3. Addition of Two 16-Bit numbers.
 4. Multiplication of two 8-bit numbers.
 5. One's Complement of an 8-bit number.
 6. Two's Complement of an 8-bit number.
 7. Find out Square Root of 0, 1, 4, 9, 16, 25, 36, 49, 64 and 81 Using Look up
 8. Display Digits 0 1 2 3 4 5 6 7 8 9 A B C D E F on the data field of screen.
 9. Rolling Display "HELP 85 up" on the address and data field of screen.
 10. Ascending order of a given set.
 11. Descending order of a given set.
 12. Multiplication by two, Employing bit rotation.
 13. Combination of two hex nibbles to form one-byte number.
 14. Separation of HEX number into two digits.
 15. Hex number stored in location for odd or even parity.
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8085

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1. Addition of two 16-bit numbers.
 2. Subtraction of two 16-bit numbers.
 3. Multiplication of two 16 Bit numbers.
 4. 32 Bit Division.
 5. Program to read a Key and display it.
 6. Search a number in an array.
 7. To find the maximum no. in a given string.
 8. To sort a string of a no. of bytes in descending order.
 9. Multiplication of ASCII string.
 10. 1's complement of a 16-bit numbers.
 11. Calculating the no. of bytes in a String.
 12. 2's complement of a 16-bit numbers.
 13. Square waveform Generation.
 14. Ramp waveform Generation.
 15. Comparing two strings.
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8086

SIXTH SEMESTER

ECL 1601

Digital Communication

L-T-P: 3-1-0

Course Outcome

Credits: 4

- CO1 Model a digital communication system.
- CO2 Compute probability of error and inter symbol interference from eye diagram in data transmission.
- CO3 Obtain the power spectra of digital modulated signals.
- CO4 Design encoder and decoder schemes for error control.

Syllabus

Waveform Coding Techniques:

Lectures: 05

Pulse code modulation: linear quantizer, quantization noise power calculation, signal to quantization noise ratio, non-uniform quantizer, A-law & μ -law, companding, encoding and bandwidth of PCM; differential pulse code modulation (DPCM), delta modulation, slope overload, adaptive delta modulation.

Digital Multiplexing:

Lectures: 03

Fundamentals of time division multiplexing, electronic commutator, bit, byte interleaving T1 carrier system, synchronization and signaling of T1, TDM, PCM hierarchy, North-American CCITT standards, T1 to T4 PCM TDM system (DS1 to DS4 signals), signal format of M12 Mux for AT & T (Bell) system, bit rate calculation for DS1 to DS4 signals.

Digital Base Band Transmission:

Lectures: 06

Line coding & its properties. NRZ & RZ types, signaling format for unipolar, polar, bipolar (AMI) & Manchester coding and their power spectra, HDB and B8ZS signaling, ISI, Nyquist criterion for zero ISI & raised cosine spectrum, matched filter receiver, derivation of its impulse response and peak pulse signal to noise ratio, correlation detector, decision threshold and error probability for binary unipolar (on-off) signaling.

Elements of Signal space Analysis:

Lectures: 04

Geometric representation of signals, Gram-Schmidt orthogonalization Technique. Coherent detection of signals in noise, correlation receiver.

Digital Modulation Techniques:

Lectures: 10

Types of digital modulation, waveforms for amplitude, frequency and phase shift keying, method of generation and detection of coherent & non-coherent binary ASK, FSK & PSK, differential phase shift keying, quadrature modulation techniques, M-ary FSK, minimum shift keying (MSK), probability of error and comparison of various digital modulation techniques. Coherent reception of ASK, PSK and FSK, non-coherent reception of ASK, FSK, PSK and QPSK, calculation of error probability of BPSK and BFSK, error probability for QPSK.

Elements of Information Theory:

Lectures: 04

Information: mutual information and channel capacity of a discrete memoryless channel, calculation of channel capacity of discrete memoryless and continuous AWGN channels, Hartely- Shannon law, bandwidth-S/N tradeoff.

Spread-spectrum modulation:

Lectures: 05

Pseudo-Noise sequence, basics of spread spectrum, direct-sequence spread-spectrum communication systems, frequency-hop spread spectrum systems, other types of spread spectrum signals.

Text Books:

1. Communication Systems, Simon Haykin & Michael Moher, 5th Edition, Wiley, 2009.
2. Modern Digital and Analog Communication Systems, B. B. P. Lathi & Zhi Ding, Oxford Univ. Press, 4th Edition, 2010.
3. Communication system engineering, J. G. Proakis and Sahhi, 2nd Edition, PHI, 2001.

Reference Books:

1. Digital Communications, John G. Proakis, Masoud Salehi, 5th Edition, T Mc Graw -Hill, 2008.
2. Digital & Analog Communication systems, K.S.Shammugham, John Wiley & Sons, 2006.
3. Principles of Digital Communication, P. Chakravarti, Dhanpat Rai & Co, 2008.
4. Wireless Digital Communication, Kamilo Feher, PHI, 1995.

ECL 1602

RF and Microwave Engineering

L-T-P: 3-1-0

Course Outcome

Credits: 4

- CO1 Recognize the limitations of existing vacuum tubes and solid-state devices at microwave frequencies.
- CO2 Study the performance of specialized microwave tubes such as klystron, reflex klystron, magnetron and Travelling wave tube.
- CO3 Understand the operation of passive waveguide components.
- CO4 Analyze microwave circuits using scattering parameters.
- CO5 Test microwave components and circuits with standard microwave bench and vector network analyzer.

Syllabus

Introduction:

Lectures: 01

Microwave frequencies, standard frequency bands, behaviour of circuits at conventional and microwave frequencies, microwave application, review of Maxwell's equations.

Transmission lines:

Lectures: 05

Transmission line theory, low loss radio frequency and UHF transmission line. UHF line as circuit element, quality factor of resonant transmission line section, the quarter wave line as a transformer, impedance matching, Smith chart.

Waveguide:

Lectures: 05

Overview of guided waves; TE and TM modes in rectangular, cut off wavelength, dominant mode, attenuation in waveguides.

Microwave Components & Devices:

Lectures: 08

N-port microwave networks. Scattering matrix representation. Reciprocal and lossless networks. Passive microwave devices: E-plane tee, H-plane tee, magic tee, attenuators, directional coupler and power dividers, resonator, ferrite devices: circulator, isolator, phase-shifter, MIC

Microwave Generators:

Lectures: 09

Transit-time effect, limitations of conventional tubes, two-cavity and multi-cavity klystrons, reflex klystron, TWT and magnetrons. Solid state devices-transferred electron devices, avalanche diode oscillator, microwave transistor, HEMT, MESFET

Microwave Measurements Technique:

Lectures: 06

Power measurement; calorimeter method, bolometer bridge method, thermocouples, impedance measurement, measurement of frequency and wavelength, measurement of unknown loads, measurement of reflection coefficient and VSWR,

Antenna Basic

Lectures: 02

RF and microwave antennas and their properties

Introduction to RADAR principles

Lectures: 02

RADAR range equation, RCS, CW and pulsed RADAR.

Text Books:

1. Microwave Engineering, D M Pozar, 4th Edition, John Wiley & Sons, 2011.
2. Microwave Devices & circuits, Liao Samuel Y., Liao, 3rd Edition, Pearson Education India, 1990.
3. Microwave, S Kar
4. Antennas by John D. Kraus
5. Introduction to RADAR systems by Skolnik

Reference Books:

1. Microwaves: Introduction to Circuits, Devices and Antennas by M L Sisodia, V. L. Gupta, New Age International, 2007.
2. Foundations of Microwave Engg., R E Collin, 2nd Edition, McGraw-Hill, 2007.
3. Microwave principles, Herbert J. Reich, Van Nostrand, 1966.
4. Microwave, K. C. Gupta, John Wiley & Sons Canada, Limited, 1980.
5. Microwave techniques, D. C. Agrawal.
6. Elements of Microwave Engineering, R. Chatterjee, Prentice Hall, 1988.
7. Microwaves Active devices vacuum and solid state, M. L. Sisodia, New age international, 2006.
8. Microwave circuits and passive devices, M. L. Sisodia, G. S. Raghuvanshi, Wiley Publisher, 1987.

ECL 1603

Computer Communication and Networks

L-T-P: 3-0-0

Course Outcome

Credits: 3

CO1 Identify the issues and challenges in the architecture of a computer network.

CO2 Understand the ISO/OSI seven layers in a network.

CO3 Realize protocols at different layers of a network hierarchy.

CO4 Recognize security issues in a network.

Syllabus

Networks and services:

Lectures: 08

Network topologies, switching methods, network evolution, concept of layered architecture, the OSI model, the TCP/IP model, standardization. Study of telephone network, PCM-TDM based IDN, circuit switching, circuit and packet switching, space and time division switching, signaling methods, store-and-forward operation: error detection and correction, ARQ strategies. Overview of ISDN, ATM networks, SONET and SDH.

MAC protocols and LANs:

Lectures: 09

ALOHA, slotted ALOHA, CSMA and CSMA-CD protocols; IEEE 802.3 protocol and MAC frame format. Details of 802.3 hardware options; 100 Mbps and 1000 Mbps Ethernet LANs, switches, bridges and VPN; Wireless LANs; LAN applications; client-server architecture.

Network Layer:

Lectures: 06

Services offered to the transport layer, internal organization as datagram or virtual circuit subnets, routing algorithms, link state and distance vector routing, congestion control, internetworking, Study of IPv4 and IP v6, DNS and Internet routing protocols such as RIP, OSPF, BGP, introduction to multiprotocol level switching

Transport Layer:

Lectures: 06

Design issues, study of TCP, connection setup and removal, flow control, reliable and efficient delivery, timer management. The TCP/IP protocol stack: ICMP, IGMP, UDP, BOOTP, DHCP etc. Examples of TCP Tahoe and TCP Reno.

Network applications:

Lectures: 07

World Wide Web and HTTP, Web servers and browsers, Content Engines, FTP and TFTP, SMTP and MIME, DNS, multimedia networking, streaming stored audio and video, Internet audio and video communications.

Network Security Techniques:

Lectures: 04

Principles of cryptography, symmetric and public key cryptography authentication, integrity, key distribution and certification, secure e-mail, Fire-walls.

Text Books:

1. Computer Networking, a top-down approach featuring the Internet, Kurose and Ross, Addison Wesley, 6e, 2012.
2. Data and Computer Communication, Stallings William, PHI, 6e, 2007.

Reference Books:

1. Computer Networks-a systems approach, Peterson and Davie- Morgan Kauffman, 2e, 2000.
2. Data communications and networking, Behrouz A. Forouzan, TMH, 4e, 2006.
3. Data Communication, Computer network & open systems, PHI, F. Halsall, 4e, 1996.
4. An Engineering approach to Computer Networking: ATM Networks, the Internet, and the Telephone Network, S. Keshav, PHI, 1e, 1997.

ECL 1604

Optoelectronics Devices and Circuits

L-T-P: 2-0-0

Course Outcome

Credits: 2

- CO1 Understand the basics physics of optoelectronics devices.
CO2 Understand and analyze the optoelectronics device based circuits.
CO3 Application of optoelectronics devices.
CO4 Explore the aspects of integrated circuit based on optoelectronics devices.

Syllabus

Elements of Light and Solid-State Physics:

Lectures: 04

Wave nature of light, Polarization, Interference, Diffraction, Light Source, review of Quantum Mechanical concept, Review of Solid-State Physics, Review of Semiconductor Junction Device.

Display Devices and Lasers:

Lectures: 07

Introduction, Photo Luminescence, Cathode Luminescence, Electro Luminescence, Injection Luminescence, LED, Plasma Display, Liquid Crystal Displays, Numeric Displays, Laser Emission, Absorption, Radiation, Population Inversion, Optical Feedback, Threshold condition, Laser Modes, Classes of Lasers, Mode Locking, laser applications

Optical Detection Devices:

Lectures: 05

Photo detector, Thermal detector, Photo Devices, Photo Conductors, Photo diodes, Detector Performance.

Optoelectronic Modulator:

Lectures: 05

Introduction, Analog and Digital Modulation, Electro-optic modulators, Magneto Optic Devices, Acousto-optic devices, Optical, Switching and Logic Devices.

Optoelectronic Integrated Circuits:

Lectures: 05

Introduction, hybrid and Monolithic Integration, Application of Optoelectronic Integrated Circuits, Integrated transmitters and Receivers, Guided wave devices.

Text Books:

1. Optoelectronics – An Introduction, J. Wilson and J.Haukes, 3rd Edition, Prentice Hall of India Pvt. Ltd., 1998

Reference Books:

1. Semiconductor Optoelectronic Devices, Pallab Bhattacharya, 2nd Edition, Prentice Hall of India Pvt., Ltd, 2004.
2. Optoelectronics – As Introduction to materials and devices, Jasprit Singh, McGraw-Hill International Edition, 1996.

CSL/ECL 16XX	Program Elective I
L-T-P: 3-0-0	Credits: 3
As per the syllabus from list of Program Elective course.	

ECP 1601**Digital Communication Laboratory****L-T-P: 0-0-3****Credits: 1.5****Experiment List:**

1. Sampling and Reconstruction of Signal.
2. Nyquist Criterion for Sampling and Reconstruction of signal.
3. Time Division Multiplexing (TDM) Transmission
4. Time Division Multiplexing (TDM) Reception
5. Delta Modulation and Demodulation.
6. Adaptive Delta Modulation.
7. Amplitude Shift Keying (ASK) Modulation and Demodulation.
8. Frequency Shift Keying (FSK) Modulation and Demodulation.
9. Binary Phase Shift Keying (BPSK) Modulation and Demodulation.
10. Quadrature Phase Shift Keying (QPSK) Modulation and Demodulation.
11. Differential Pulse Code Modulation and Demodulation.
12. 8-QAM (Quadrature Amplitude Modulation) Modulation and Demodulation.
13. Data Formatting & Carrier Transmitter.
14. Data Formatting & Carrier Receiver.
15. Study of Analog to Digital Conversion.

ECP 1602**RF & Microwave Engineering Laboratory****L-T-P: 0-0-3****Credits: 1.5****Experiment List:**

1. Study of Reflex Klystron Characteristics.
2. Frequency and Wavelength Measurement using Reflex Klystron.
3. Impedance Measurement of Unknown Microwave Load.
4. Determination of Polar Radiation Patterns of Horn Antenna.
5. Determination of Gain of Microwave Antenna.
6. Study of I-V characteristics of Gunn Oscillator.
7. Study of Output Power and Frequency as a Function of Bias Voltage.
8. Study of Tuning Characteristics of Gunn Diode as a Function of Micrometer Screw Position.
9. Study and Characterization of Ferrite Isolator.
10. Study and Characterization of Waveguide Based 3-port Circulator.
11. Study and Characterization Waveguide Based of Attenuator.
12. Study and Characterization of Waveguide Based Magic Tee.
13. Study and Characterization of Waveguide Based Directional Coupler.
14. Determination of Dielectric Constant of Solid Plate of Unknown Material.
15. Determination of Thickness of Solid Plate.

ECD 1601	Industrial Training and Seminar
L-T-P: 0-0-2	Credits: 1
As per the training of minimum 4 weeks undergone by the student(s) in the recess of 5 th Semester.	

SEVENTH SEMESTER

ECL 1701

Optical Communication and Network

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Identify and characterize different components of an Optical Fiber Communication link.
- CO2 Analyze optical source, Fiber and Detector operational parameters
- CO3 Compute optical fiber link design parameters
- CO4 Understand WDM, Optical Amplifiers, Optical Switching and networking technology concepts.

Syllabus

Introduction:

Lectures: 10

Fundamental of fiber optics, different generations of optical fiber communication system, optical fiber structure, fiber types. modes in optical fiber signal degradation in optical fibers, fiber losses.

Power launching and coupling:

Lectures: 12

Source to fibre power launching, lensing schemes for coupling improvement, fibre to fibre joints, couplers, multiplexers and splices. Conversion of electric signal to optical signal and vice versa.

Optical fiber systems:

Lectures: 06

Intensity modulation/direct detection system, link budget using direct detection, coherent system, wavelength converters, coherent and WDM systems, EDFA, Photonic switching.

Optoelectronic Modulator

Lectures: 08

Introduction, Analog and Digital Modulation, Electro-optic modulators, Magneto Optic Devices, Acousto-optic devices, Optical, Switching and Logic Devices.

Microwave Photonics

Lectures: 04

Frequency down conversion and up conversion

Text Books:

1. Optical Fiber Communications, G. Keiser, McGraw Hill, 4e, 2010.
2. Optical Fiber Communications: Principles and Practice, John M. Senior, PHI, 3e, 2008.
3. Fiber Optics communication system, G. P. Agrawal, John Willy & Sons, 1992

Reference Books:

1. Introduction to Optical Fiber Communications Systems, Jones, William B. Jones, Oxford University Press, 1995.
2. Understanding Optical Fiber Communications, A. J. Rogers, Artech House, 2001
3. Fiber optic communication, J. C. Palais, Prentice Hall, 5e, 2004.
4. Optical fiber & Fiber Optical Communication Systems, Subir Kumar Sarkar, S. Chand, 2007.

ECL 1702

Wireless Communication

L-T-P: 3-0-0

Course Outcome

Credits:3

- CO1 Understand the evolution of cellular communication systems up to and beyond 3G
- CO2 Design a cellular link and estimate the power budget.
- CO3 Choose proper multiple accessing methods depending on channel model
- CO4 Identify traffic channels for call processing
- CO5 Calculate key performance metrics of a cellular communication system.

Syllabus

Wireless Communication Systems & Standards:

Lectures: 07

Evolution of Mobile Radio Communications, Cellular telephone systems, Different generations (1G to 4G) of wireless communication and Networks; GSM, GPRS, EDGE, CDMA, UMTS, WLAN, WLL, Bluetooth, PAN, Recent advances in mobile computing.

Propagation & Fading:

Lectures: 08

Review of Path losses in indoor and outdoor propagation channels, Multipath fading, Doppler shift, time and frequency dispersive channels, delay spread and coherence bandwidth, flat and frequency selective fading, slow and fast fading, coherence time, LCR and ADF.

The Cellular Concept:

Lectures: 06

Frequency Assignment and Channel Assignment, Frequency Reuse, Handoff, Sectoring, Repeaters for range extension, Microcell zone, Spectral efficiency & capacity of cellular systems.

Mobile Radio Interferences & System Capacity:

Lectures: 05

Co-channel Interference and System Capacity, Channel planning for Wireless Systems, Adjacent channel interferences, Power control for reducing interference, Near-end-to-far-end interference, Inter-symbol and Simulcast interference, False alarm rate and word error rate.

Multiple access schemes:

Lectures: 03

FDMA, TDMA, CDMA and SDMA.

Diversity & Combining Techniques:

Lectures: 07

Diversity Schemes (Space, frequency, field and polarization diversities) and combining techniques, diversity receivers- selection, MRC & EGC. RAKE receiver, equalization linear- ZFE and adaptive, DFE.

Antennas for wireless communication:

Lectures: 04

Antennas used for Mobile Communications, Radiation patterns, antennas for mobile terminal, base station antennas, Smart antenna (basic concept).

Text Books:

1. Wireless Communication: Principles & Practice, T. S. Rappaport, Prentice Hall, 2e, 2002.

Reference Books:

1. Mobile Cellular Telecommunications Systems, W. C. Y. Lee, TMH, 2e, 2002.
2. Wireless communication, Andrea Goldsmith, Cambridge University press, 2005.

ECL 17XX	Program Elective II
L-T-P: 3-0-0	Credits: 3
As per the syllabus from list of Program Elective course.	

ECL 17XX	Program Elective III
L-T-P: 3-0-0	Credits: 3
As per the syllabus from list of Program Elective course.	

ECD 1701	Project Phase – I
L-T-P: 0-0-10	Credits: 5
As per the project decided by the student and concerned supervisor.	

ECD 1702	Seminar
L-T-P: 0-0-2	Credits: 1
Each student will be assigned a topic for Seminar (other than the topic of Project).	

EIGHTH SEMESTER

ECL 18XX	Program Elective IV
L-T-P: 3-0-0	Credits: 3
As per the syllabus from list of Program Elective course.	

CHL 1802	Environmental Science
L-T-P: 2-0-0	AUDIT
As per the syllabus prescribed by Dept. of Basic Science & Humanities and Social Science.	

EEL/CSL/MAL 18XX	Open Elective
L-T-P: 3-0-0	Credits: 3
As per the syllabus from list of Open Elective course.	

ECP 1801**VHDL Design Laboratory****L-T-P: 0-0-2****Credits: 1****Experiment List:**

1. Basic Gates, Universal Gates using target device
2. Adders - Half & Full adder
3. Subtractors – Half & Full subtractor
4. Code Convertors
5. Multiplexer – 8:1, 32:1
6. Demultiplexer – 1:8, 1:32
7. Decoder – 2:4, 3:8, 4:16
8. Encoder – 16:4, 8:3, 4:2
9. Up & Down Counters
10. Positive level triggered D-latch
11. Positive edge-triggered T-Flip-flop
12. Positive edge-triggered JK-Flip-flop
13. Comparator - 4 bit
14. Shift registers (SISO, PISO)

ECD 1801	Project Phase – II
L-T-P: 0-0-20	Credits: 10
As per the project decided by the student and concerned supervisor and continuation of Project Phase-I.	

ECD 1802	Grand Viva
L-T-P: 0-0-2	Credits: 1
Each student will present themselves for Viva (as a part of their assessment) for all the courses in entire B.Tech programme.	

Program Elective(s) [PE I]

ECL 1XXX

Antenna Engineering

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Understand the concept of radiation through mathematical formulation
- CO2 Plot the characteristics of wire and aperture antennas
- CO3 Develop the performance characteristics of array antennas
- CO4 Measure the antenna parameters
- CO5 Understand the behavior of nature on em wave propagation

Syllabus

Antenna Basics:

Lectures: 08

Introduction, Radiation Mechanism, Antenna Parameters-Radiation Patterns, Patterns in Principle Planes, Main Lobe and Side Lobes, Beam widths, Beam Area, Radiation Intensity, Beam Efficiency, Directivity, Gain and Resolution, Antenna Apertures, Aperture Efficiency, Effective Height, Antenna Theorems- Applicability and Proofs for equivalence of directional characteristics.

Radiation from Wires:

Lectures: 07

Retarded Potentials, Small Electric Dipole, Quarter wave Monopole and Half wave Dipole Radiation characteristics.

Antenna Arrays:

Lectures: 07

Two element array, Principle of Pattern Multiplication, N element Uniform Linear Arrays - Broadside, End fire Arrays, EFA with Increased directivity, Binomial Arrays, Methods of Array synthesis- Tchebyscheff Distribution and Fourier Transform Method.

HF, VHF and UHF Antennas:

Lectures: 07

Traveling wave radiators –basic concepts, Long wire antennas-field strength calculations and patterns, V-antennas, Rhombic Antennas and Design Relations, Small Loop antennas- Concept of short magnetic dipole, Helical Antennas, Yagi-Uda Arrays, Log periodic antennas.

Microwave Antennas and Antenna Measurement Pattern:

Lectures: 08

Reflector antennas, Flat Sheet and Corner Reflectors, Paraboloidal Reflectors, Cassegrain Feeds. Slot antennas-Babinet's principle, Microstrip antennas, and Horn antennas, Lens antennas (Qualitative treatment only), Antenna Measurements-Patterns Required, Set Up, Distance Criterion, Directivity and Gain Measurements (Comparison, Absolute and Antenna Methods).

Wave Propagation:

Lectures: 10

Concepts of Propagation- frequency ranges and types of propagations. Ground Wave propagation - characteristics, Parameters, Wave Tilt, Flat and Spherical Earth Considerations, Sky Wave Propagation-Formation of Ionospheric Layers and their characteristics, Mechanism of Reflection and Refraction, Critical Frequency, MUF & Skip Distance Calculations for flat and spherical earth cases, Optimum Frequency, Virtual Height, Ionospheric Abnormalities, Ionospheric Absorption, Fundamental Equation for Free-Space Propagation, Basic Transmission Loss Calculations, Space Wave Propagation - Mechanism, LOS and Radio Horizon, Tropospheric Wave Propagation- Radius of Curvature of path, Effective Earth's Radius, Effect of Earth's Curvature, Field Strength Calculations, M-Curves and Duct Propagation, Tropospheric Scattering.

Text Books:

1. Antennas and Radio Wave Propagation, R. E. Collin, McGraw – Hill, 1985.
2. Antennas and Wave Propagation, K. D. Prasad, Tech Publications, 3rd edition, 2001.
3. Antennas and Wave Propagation, G. S. N Raju, 1st Edition, Pearson Education, 2004.

Reference Books:

1. Antenna Theory and Practice, R. Chatterjee, New age Publisher, 2004.
2. Antenna by J. D. Kraus, Tata McGraw Hil, 2006.
3. Principles of Antenna Theory, K. F. Lee, Wiley, 1984.
4. Antenna theory, analysis and design, Constantine Balanis, 2nd edition, John Wiley & Sons, 2012.

ECL 1XXX

Low Power VLSI Design

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Identify the sources of power consumption in a given VLSI Circuit
- CO2 Analyze and estimate dynamic, leakage power components in a DSM VLSI circuit
- CO3 Choose SRAMs/DRAMs for Low power applications
- CO4 Design low power arithmetic circuits and systems
- CO5 Decide at which level of abstraction it is advantageous to implement low power techniques in a VLSI system design

Syllabus

Low power Basics:

Lectures: 03

Need for low power VLSI chips, Sources of power dissipation on Digital.

Integrated circuits:

Lectures: 03

Emerging Low power approaches. Physics of power dissipation in CMOS devices, Subthreshold Circuit Design.

Device & Technology Impact on Low Power:

Lectures: 04

Dynamic dissipation in CMOS, Transistor sizing & gate oxide thickness, Impact of technology Scaling, Technology & Device innovation.

Power estimation Simulation Power analysis:

Lectures: 05

Gate level logic simulation, capacitive power estimation, static state power, gate level capacitance estimation, architecture level analysis.

Low Power Design Circuit level:

Lectures: 05

Power consumption in circuits. Flip Flops & Latches design, high capacitance nodes, low power digital cells library.

Logic level:

Lectures: 04

Gate reorganization, signal gating, logic encoding, state machine encoding, pre-computation logic.

Low power Architecture & Systems:

Lectures: 06

Power & performance management, switching activity reduction, parallel architecture with voltage reduction, flow graph transformation, low power arithmetic components, low power memory design.

Leakage Power minimization Approaches:

Lectures: 04

Variable-threshold-voltage CMOS (VTCMOS) approach, Multi-threshold-voltage CMOS (MTCMOS) approach, Power gating, Transistor stacking, Dual-Vt assignment approach (DTCMOS).

Algorithm & architectural level methodologies:

Lectures: 04

Introduction, design flow, Algorithmic level analysis & optimization, Architectural level estimation & synthesis.

Text Books:

1. Low-Power CMOS VLSI Circuit Design, Kaushik Roy, Sharat and C. Prasad, John Wiley & Sons, 2009.
2. Digital integrated circuits: a design perspective, Jan M. Rabaey, Anantha P. Chandrakasan, Borivoje Nikolic, 2nd Edition, Pearson Education, 2003.

Reference Books:

1. Practical Low Power Digital VLSI Design, Gary K. Yeap, Springer London, Limited, 1998.
2. Low power design methodologies, Jan M. Rabaey, Massoud Pedram, 2nd Edition, Kluwer Academic Publishers, 1996.

CSL 1XXX		Software Engineering	
L-T-P: 3-0-0			Credits: 3
As per the syllabus prescribed by the Department of Computer Science & Engineering.			

Program Elective(s) [PE II – IV]

ECL 1XXX

Foundations of MEMS

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Understand biosensing and transducing techniques
- CO2 Understand principles of linking cell components and biological pathways with energy transduction, sensing and detection
- CO3 Demonstrate appreciation for the technical limits of performance of biosensor
- CO4 Apply principles of engineering to develop bio analytical devices and design of biosensors

Syllabus

Scaling Laws, Why MEMS?

Lectures: 02

Microfabrication Techniques:

Lectures: 06

Bulk micro machining, surface micro machining and LIGA processes.

MEMS based inertial sensors:

Lectures: 06

Accelerometer; piezoresistive and capacitive.

MEMS based gyro and tilt sensors

Lectures: 02

MEMS based pressure sensor – Tyre Pressure Monitoring System

Lectures: 02

Electrostatic actuation:

Lectures: 04

Study of electrostatically actuated micro-machined cantilever beam: Free natural mode of vibration, resonance analysis, static voltage response, pull in and pull-out phenomenon. Dynamic response to time varying electrostatic actuation

RF MEMS:

Lectures: 06

RF switch, MEMS based inductor and capacitors, MEMS based varactors and resonators.

Optical MEMS:

Lectures: 02

MEMS based mirrors; MEMS based optical switch

Microfluidic and Bio MEMS:

Lectures: 01

Advantages of MEMS based fluidic system.

Micro pump and Microvalve:

Lectures: 06

Micro pump and Micro valve, Micro nozzle and thrusters, micro needle, micro cantilever-based bio sensors, lab on a chip

MEMS based interfacing electronics:

Lectures: 04

Variable gain instrumentation amplifier and wireless integrated micro sensors.

Text Books:

1. Analysis and design principles of MEMS devices by M. H. Bao.
2. Microsystem Design by Stephen D. Senturia, Kluwer Academic Publishers, 2001.
3. Micro and Smart system by G. K. Ananthasuresh, K. J. Vinoy, S. Gopalakrishnan, K. N. Bhat, V. K. Aatre, Wiley, 2012.
4. Fundamentals of Microfabrication techniques, Marc Madou, CRC Press

Reference Books:

1. Micromachined Transducers Sourcebook, Gregory T. A. Kovacs, WCB/MacGraw-Hill, 1998.
2. Micromechanical Transducers: Pressure sensors, accelerometers, and gyroscopes, M-H. Bao, Elsevier publisher, NewYork, 2000.
3. MEMS, Vijay Vardan, Wiley Publication.
4. MEMS and Microsystems Design and Manufacture, Tai- Ran Hsu, TMH, 2002.
5. MEMS, Nitaigour Mahalik, Tata McGraw-Hill Education, 2008.
6. MEMS and MOEMS Technology and Applications, Rai Chaoudhary, PHI Learning, 2000.

ECL 1XXX

Antenna Engineering

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Understand the concept of radiation through mathematical formulation
- CO2 Plot the characteristics of wire and aperture antennas
- CO3 Develop the performance characteristics of array antennas
- CO4 Measure the antenna parameters
- CO5 Understand the behavior of nature on em wave propagation

Syllabus

Antenna Basics:

Lectures: 08

Introduction, Radiation Mechanism, Antenna Parameters-Radiation Patterns, Patterns in Principle Planes, Main Lobe and Side Lobes, Beam widths, Beam Area, Radiation Intensity, Beam Efficiency, Directivity, Gain and Resolution, Antenna Apertures, Aperture Efficiency, Effective Height, Antenna Theorems- Applicability and Proofs for equivalence of directional characteristics.

Radiation from Wires:

Lectures: 07

Retarded Potentials, Small Electric Dipole, Quarter wave Monopole and Half wave Dipole Radiation characteristics.

Antenna Arrays:

Lectures: 07

Two element array, Principle of Pattern Multiplication, N element Uniform Linear Arrays - Broadside, End fire Arrays, EFA with Increased directivity, Binomial Arrays, Methods of Array synthesis- Tchebyscheff Distribution and Fourier Transform Method.

HF, VHF and UHF Antennas:

Lectures: 07

Traveling wave radiators –basic concepts, Long wire antennas-field strength calculations and patterns, V-antennas, Rhombic Antennas and Design Relations, Small Loop antennas- Concept of short magnetic dipole, Helical Antennas, Yagi-Uda Arrays, Log periodic antennas.

Microwave Antennas and Antenna Measurement Pattern:

Lectures: 08

Reflector antennas, Flat Sheet and Corner Reflectors, Paraboloidal Reflectors, Cassegrain Feeds. Slot antennas-Babinet's principle, Microstrip antennas, and Horn antennas, Lens antennas (Qualitative treatment only), Antenna Measurements-Patterns Required, Set Up, Distance Criterion, Directivity and Gain Measurements (Comparison, Absolute and Antenna Methods).

Wave Propagation:

Lectures: 10

Concepts of Propagation- frequency ranges and types of propagations. Ground Wave propagation-characteristics, Parameters, Wave Tilt, Flat and Spherical Earth Considerations, Sky Wave Propagation-Formation of Ionospheric Layers and their characteristics, Mechanism of Reflection and Refraction, Critical Frequency, MUF & Skip Distance Calculations for flat and spherical earth cases, Optimum Frequency, Virtual Height, Ionospheric Abnormalities, Ionospheric Absorption, Fundamental Equation for Free-Space Propagation, Basic Transmission Loss Calculations, Space Wave Propagation - Mechanism, LOS and Radio Horizon, Tropospheric Wave Propagation- Radius of Curvature of path, Effective Earth's Radius, Effect of Earth's Curvature, Field Strength Calculations, M-Curves and Duct Propagation, Tropospheric Scattering.

Text Books:

1. Antennas and Radio Wave Propagation, R. E. Collin, McGraw – Hill, 1985.
2. Antennas and Wave Propagation, K. D. Prasad, Tech Publications, 3rd edition, 2001.
3. Antennas and Wave Propagation, G. S. N Raju, 1st Edition, Pearson Education, 2004.

Reference Books:

1. Antenna Theory and Practice, R. Chatterjee, New age Publisher, 2004.
2. Antenna by J. D. Kraus, Tata McGraw Hil, 2006.
3. Principles of Antenna Theory, K. F. Lee, Wiley, 1984.
4. Antenna theory, analysis and design, Constantine Balanis, 2nd edition, John Wiley & Sons, 2012.

ECL 1XXX**Information Theory and Coding****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Understand the basic concepts of information theory, mutual information and characteristics of various types of communication noisy channels
- CO2 Discuss the concepts of entropy, channel capacity and various source coding schemes
- CO3 Discuss the various channel coding techniques for error-free transmission of message over a noisy channel
- CO4 Understand the basic techniques of protecting information from unauthorized access and concepts of cryptography

Syllabus**Introduction:****Lectures: 25**

Entropy and mutual information for discrete ensembles, source coding, variable length coding, discrete memoryless channels, mutual information, channel capacity, channel coding theorem, differential entropy and mutual information for continuous ensembles, channel capacity for Gaussian channels, channel coding, linear block codes, and cyclic codes, convolution codes, sequential and probabilistic decoding, majority logic decoding, burst error-correcting codes, turbo codes and low-density-parity check codes.

Cryptography:**Lectures: 15**

Basic concepts on cryptography and crypto analysis, security issues; private-key encryption algorithms-stream ciphers, block ciphers, Shannon's theory; introduction to number theory – modular arithmetic, exponentiation and discrete logarithms in Galois field; public key encryption algorithms-Diffie-Hellman public-key distribution scheme, RSA public-key cryptosystem; Message authentication, hashing functions, digital signatures.

Text Books:

1. Communication Systems, S. Haykin, 4th Edition, John Wiley & Sons, New York, 2001.
2. Elements of Information Theory, T M Cover and J A Thomas, John Wiley, 1991.
3. Information Theory, Coding and Cryptography, R. Bose, Tata McGraw-Hill, 2002.

Reference Books:

1. Introduction to Cryptography with Coding Theory, Wade Trappe, Lawrence C. Washington, 2nd Edition, Pearson Education India, 2007.
2. Modern digital and Analog communications, B. P. Lathi, 3rd Edition, Oxford University Press, 2000.
3. Cryptography: Theory and Practice, D. R. Stinson, 3rd Edition, Champmen & Hall/CRC, 2006.

ECL 1XXX

Sensors and Instrumentation

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Measure displacement using capacitive and resistive transducers.
- CO2 Measure temperature and strain using appropriate transducers
- CO3 Build a simple data acquisition system using DMM.
- CO4 Control DMM and DSO via GP-IB and perform measurements of sensor signals

Syllabus

General Concepts of Measurement:

Lectures: 10

Generalized Measurement System – Performance Characteristics – Static and Dynamic Characteristics – Errors in Measurements. Transducers and their Classifications-Sensor characteristics-emerging fields of sensor technology-basic principle of resistive, capacitive, inductive, piezoelectric transducers, Hall Effect sensors and their applications

Sensor Applications:

Lectures: 06

Introduction - Acceleration Sensors-Force Measurement - Torque and Power Measurement-Flow Measurement - Temperature Measurements-Distance Measuring and Proximity Sensors-Light sensor.

Signal Conditioning:

Lectures: 10

Signal conditioning requirements: drift, noise, bandwidth, signal-to-noise ratio. Instrumentation amplifier, charge amplifier, Wheatstone bridge integration, differentiation and sampling, A/D and D/A conversion, choppers, voltage to time ADC, voltage to frequency conversion.

Data Acquisition:

Lectures: 07

Introduction to real-time interfacing: Elements of data acquisition and control overview of I/O process, Data Acquisition conversion, General configuration: single channel and multichannel, Data Logging, Data conversion, Digital Transmission.

Virtual Instrumentation:

Lectures: 07

Introduction to LabVIEW, Block diagram and architecture of the virtual instrumentation, data flow techniques, graphical programming, VIS and sub-VIS, loops & charts, arrays, clusters, graphs, case & sequence structures, formula modes, local and global variable, string & file input.

Text Books:

1. D. Helfric and W.D. Cooper, "Modern Electronic Instrumentation and measuring techniques.", PHI, 1990.
2. E. Jones, "Instrumentation, measurement and feedback", Tata Mc Graw-Hill, 1986.
3. E. O. Deobelin, "Measurement Systems - Applications and design", Tata Mc Graw-Hill, 1990.
4. Sensors and Transducers, Patranabis, D, Wheeler Publishing Co, Ltd., New Delhi, 1997.

Reference Books:

1. F. Coombs, "Electronics Instruments Handbook", Tata Mc Graw-Hill, 1995
 2. R. P. Areny and T. G. Webster, "Sensor and Signal Conditioning", John Wiley, 1991
 3. B. M. Oliver and J. M. Cage, "Electronic Measurements and Instrumentation", Tata MGH, 1975
 4. Industrial Instrumentation and Control, Buchanan, W, Butterworth-Heinemann Publishers, 1999.
- PC Interfacing for Data Acquisition and Process Control, ISA, S. Gupta and J.P. Gupta, 2e, 1994.

ECL 1XXX

Low Power VLSI Design

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Identify the sources of power consumption in a given VLSI Circuit
- CO2 Analyze and estimate dynamic, leakage power components in a DSM VLSI circuit
- CO3 Choose SRAMs/DRAMs for Low power applications
- CO4 Design low power arithmetic circuits and systems
- CO5 Decide at which level of abstraction it is advantageous to implement low power techniques in a VLSI system design

Syllabus

Low power Basics:

Lectures: 03

Need for low power VLSI chips, Sources of power dissipation on Digital.

Integrated circuits:

Lectures: 03

Emerging Low power approaches. Physics of power dissipation in CMOS devices, Subthreshold Circuit Design.

Device & Technology Impact on Low Power:

Lectures: 04

Dynamic dissipation in CMOS, Transistor sizing & gate oxide thickness, Impact of technology Scaling, Technology & Device innovation.

Power estimation Simulation Power analysis:

Lectures: 05

Gate level logic simulation, capacitive power estimation, static state power, gate level capacitance estimation, architecture level analysis.

Low Power Design Circuit level:

Lectures: 05

Power consumption in circuits. Flip Flops & Latches design, high capacitance nodes, low power digital cells library.

Logic level:

Lectures: 04

Gate reorganization, signal gating, logic encoding, state machine encoding, pre-computation logic.

Low power Architecture & Systems:

Lectures: 06

Power & performance management, switching activity reduction, parallel architecture with voltage reduction, flow graph transformation, low power arithmetic components, low power memory design.

Leakage Power minimization Approaches:

Lectures: 04

Variable-threshold-voltage CMOS (VTCMOS) approach, Multi-threshold-voltage CMOS (MTCMOS) approach, Power gating, Transistor stacking, Dual-Vt assignment approach (DTCMOS).

Algorithm & architectural level methodologies:

Lectures: 04

Introduction, design flow, Algorithmic level analysis & optimization, Architectural level estimation & synthesis.

Text Books:

3. Low-Power CMOS VLSI Circuit Design, Kaushik Roy, Sharat and C. Prasad, John Wiley & Sons, 2009.
4. Digital integrated circuits: a design perspective, Jan M. Rabaey, Anantha P. Chandrakasan, Borivoje Nikolic, 2nd Edition, Pearson Education, 2003.

Reference Books:

3. Practical Low Power Digital VLSI Design, Gary K. Yeap, Springer London, Limited, 1998.
4. Low power design methodologies, Jan M. Rabaey, Massoud Pedram, 2nd Edition, Kluwer Academic Publishers, 1996.

ECL 1XXX**Nano Electronics****L-T-P: 3-0-0****Course Outcome****Credits: 3**

CO1 Introduction of the basics of quantum theory and its applications in Electronics

CO2 Understanding of CMOS scaling and development of novel nanodevices

CO3 Understand the fundamentals of nanodevice using molecular theory

Syllabus**Principles of Quantum Mechanics:****Lectures: 10**

Wave nature of particles and wave-particle duality, Pauli-exclusion principle, wave functions and Schrodinger's equations. Transport mechanisms: drift, diffusion, and Ballistic. Quantum dots, wires, and wells. Principles of optical devices, geometry as the material parameter

Scaling approaches:**Lectures: 15**

CMOS scaling: advantages and limitations. Nanoscale MOSFETs, FINFETs, Vertical MOSFETs, system integration limits (interconnect issues etc.), Tunneling Transistors, Single electron transistors, spintronics, and Junctionless Transistor.

Bottom-up approaches:**Lectures: 15**

Single molecules as electronic devices, transport in molecular structures, molecular systems as alternatives to conventional electronics, molecular interconnects; Carbon nanotube electronics, band structure, applications.

Text Books:

1. Solid State Physics, Ashcroft and Mermin, Thomson Press (India) Ltd, 2003
2. Introduction to Nanotechnology, C.P. Poole Jr., F. J. Owens, Wiley, 2003.
3. Fundamentals of Nanoelectronics, G. W. Hanson, Pearson, 2007.

Reference Books:

1. Nanosystems: molecular, machinery, manufacturing and computation, K.E. Drexler, Wiley, 1992.
2. The Physics of Low-Dimensional Semiconductors, John H. Davies, Cambridge University Press, 1998.
3. Nanoelectronics and Information Technology (Advanced Electronic Materials and Novel Devices), Waser Ranier, Wiley-VCH, 3e, 2012.
4. Introduction to Solid State Physics, C. Kittel, Wiley, 2012

ECL 1XXX**Radar Communication****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Understand the basic operation of pulse and CW radar systems.
- CO2 Evaluate the radar performance based on pulse width, peak power and beam width.
- CO3 Choose suitable tracking radar for a given problem.
- CO4 Select appropriate criterion for detecting a target.
- CO5 Understand the working of phased array radars and navigational aids

Syllabus**Introduction:****Lectures: 06**

Principle of detection and ranging, Radar frequencies and bands, applications, radar block diagram and operation.

Radar range equation:**Lectures: 14**

Range prediction, minimum detectable signal, receiver noise and SNR, noise temperature, pattern propagation factor, antenna gain, loss factors, jamming & clutter, accuracy of prediction, integration of radar pulses, radar cross section of targets, transmitter power, PRF and range ambiguities, system losses & propagation effects.

CW FM radar:**Lectures: 06**

Doppler Effect, CW radar, frequency-modulated CW radar, multiple-frequency CW radar.

MTI and Pulse Doppler radar:**Lectures: 14**

MTI delay lines, delay line cancellers, coherent and non-coherent mti, pulse Doppler radar, monopulse RADAR, diplexer, duplexer, phased array RADAR, digital beam forming RADAR

Text Books:

1. Introduction to Radar Systems, M. I. Skolnik, McGrawhill, 2004.
2. Introduction to Airborne RADAR, Stimson

Reference Books:

1. Radar Engineering, D. G. Rink.
2. Principles of Radar and Sonar Signal Processing, Francois Le Chevalier, Artech House, 2002.

ECL 1XXX**CAD for VLSI****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Distinguish between DSP and FPGA based filter architectures
- CO2 Compare the architectures of general-purpose processors and DSP processors
- CO3 Design simple IP cores for FPGA applications
- CO4 Use the CAD tools to model an FPGA design
- CO5 Model and design a heterogeneous FPGA based embedded system

Syllabus**Introduction:****Lectures: 04**

VLSI design flow, Full-custom, standard-cell, gate-array and FPGA, VLSI Design automation tools.

Basic concepts of high-level synthesis:**Lectures: 04**

Partitioning, scheduling, allocation and binding.

Verilog / VHDL:**Lectures: 10**

Introduction and use in synthesis, modeling combinational and sequential logic, Procedures, assignments and control statements in Verilog, writing test benches.

Technology mapping and Test-ability issues:**Lectures: 08**

Fault modeling and simulation, test generation, design for test-ability, built-in self-test, Testing SoC's, Basic concepts of verification, Silicon Compiler.

Algorithmic Graph Theory:**Lectures: 04**

Data structure for graph representation, graph algorithms – depth first search, breadth first search, Dijkstra's shortest path, Prim's algorithm.

Physical design automation algorithms:**Lectures: 10**

Floor-planning, placement, routing, layout synthesis, design rule check, compaction, power and delay estimation, clock and power routing, etc. Special considerations for analog and mixed-signal designs.

Text Books:

1. VHDL: Programming by Example, Douglas Perry, McGraw-Hill Professional; 4 ed., 2002.
2. Verilog HDL, Samir Palnitkar, 2 ed, Pearson, 2004.
3. Algorithms for VLSI physical design automation, N. A. Sherwani, Kluwer Academic Publishers, 1999.

Reference Books:

1. Algorithms for VLSI Design Automation, S. H. Gerez, Wiley-India, 1999.
2. Synthesis and Optimization of Digital Circuits, Giovanni De Micheli, Tata McGraw Hill, 1994.
3. VLSI physical design automation: theory and practice, S. M. Sait and H. Youssef, World Scientific Pub. Co., 1999.
4. RTL Hardware Design using VHDL: Coding for efficiency, Portability, and Scalability, Pong P. Chu, John Wiley- & Sons Inc., Hoboken, New Jersey, 2006.
5. An Introduction to Physical Design, M. Sarrafzadeh and C. K. Wong, McGraw Hill, 1996.

ECL 1XXX**Digital Image Processing****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Understand the need for image transforms and their properties.
- CO2 Choose appropriate technique for image enhancement both in spatial and frequency domains.
- CO3 Identify causes for image degradation and apply restoration techniques.
- CO4 Compare the image compression techniques in spatial and frequency domains.
- CO5 Select feature extraction techniques for image analysis and recognition.

Syllabus**Introduction:****Lectures: 02**

Fundamental steps in digital image processing, components of an image processing system.

Digital Image Fundamentals:**Lectures: 04**

Image sampling and quantization, some basic relationships between pixels, linear and nonlinear operations.

Image enhancement in spatial domain:**Lectures: 06**

Some basic gray level transformations, Histogram processing, Smoothing and Sharpening spatial filters.

Image enhancement in frequency domain:**Lectures: 05**

Smoothing and sharpening frequency domain filters, homo-morphic filtering.

Image segmentation:**Lectures: 06**

Detection of discontinuities, edge linking and boundary detection, thresholding, region-based segmentation, recent developments.

Image restoration:**Lectures: 06**

Noise models, restoration in the presence of noise only-spatial filtering, estimating the degradation functions, inverse filtering.

Colour Image processing:**Lectures: 02**

Color models, pseudo-color processing.

Image compression:**Lectures: 04**

Image compression models, loss-less and lossy compression.

Morphological image processing:**Lectures: 05**

Dilation and erosion, opening and closing, some basic morphological algorithms.

Text Books:

1. Digital Image Processing, R. C. Gonzalez and R. E. Woods, Pearson Education, 2006.
2. Digital picture Processing, A. Rosenfield & A. C. Kak.

Reference Books:

1. Fundamentals of Digital Image Processing, K. Jain, Pearson Education, 2007.
2. Theory and Application of Digital Signal Processing, L. R. Rabiner and B. Gold, Pearson Education, 2004.

ECL 1XXX**Satellite Communication****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Understand the orbital and functional principles of satellite communication systems
- CO2 Architect, interpret, and select appropriate technologies for implementation of specified satellite communication systems
- CO3 Analyse and evaluate a satellite link and suggest enhancements to improve the link performance.
- CO4 Select an appropriate modulation, multiplexing, coding and multiple access schemes for a given satellite communication link.
- CO5 Specify, design, prototype and test analog and digital satellite communication systems as per given specifications

Syllabus**Introduction:****Lectures: 05**

Satellite communication system, communications satellites, different orbits, frequency bands, and satellite multiple access formats.

Satellite communication channel:**Lectures: 05**

Power flow, polarization, atmospheric losses, receiver noise, CNR, satellite link analysis for uplinks and downlinks.

Satellite transponder:**Lectures: 08**

Transponder model, satellite signal processing RF-RF translation, IF demodulation.

Multiple access techniques:**Lectures: 10**

Frequency division multiple access, amplification with multiple FDMA carriers, AM/FM conversion with FDMA, switched FDMA, synchronization, SS-time division multiple access, code division multiple access, DS CDMA, frequency-hopped, CDMA.

Satellite link design:**Lectures: 07**

Performance requirements and standards. Design of satellite links – DOMSAT, INSAT, INTELSAT and INMARSAT, satellite- based personal communication.

Earth station design:**Lectures: 05**

Configuration, antenna and tracking systems, satellite broadcasting.

Text Books:

1. Satellite Communication, D. Roddy, Mc Graw- Hill, 3rd edition, 2001.
2. Satellite Communications, T. Pratt and W. Boston, John Wiley & Sons, 2004.
3. Digital Satellite Communications, T. T. Ha, McGraw Hill, U. S. A., 2004.

Reference Books:

1. Satellite Communications, Gagliardi.
2. Satellite Communications system using design principles, M. Richharia.
3. Principles of Communication Satellite, G. D. Gordon, W. L. Morgan, John Wiley & Sons, U. S. A., 2005.

ECL 1XXX

Wireless Sensors Networks

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Identify the components of Wireless Sensor Networks
- CO2 Understand the challenges in network coverage and routing for energy efficiency
- CO3 Define node Architecture for specific applications
- CO4 Program sensor network platforms using specialized operating system
- CO5 Recognize upcoming challenges in Sensor Networks.

Syllabus

Overview of wireless sensor networks:

Lectures: 02

Introduction to Adhoc networks, mobile adhoc networks and sensor networks. Challenges for wireless sensor networks, enabling technologies for wireless sensor networks.

Architectures:

Lectures: 08

Single node architecture, hardware components, energy consumption of sensor nodes, operating systems and execution environments, network architecture, sensor network scenarios, optimization goals and figures of merit, gateway concepts

Networking sensors:

Lectures: 10

Physical layer and transceiver design considerations, MAC protocols for wireless sensor networks, low duty cycle protocols and wakeup concepts, S-MAC, the mediation device protocol, wakeup radio concepts, address and name management, assignment of MAC addresses, routing protocols, energy, efficient routing, geographic routing.

Infrastructure establishment:

Lectures: 10

Energy efficient topology, Clustering- LEACH, PEGASIS, ELBERA etc. Time synchronization, localization and positioning, sensor tasking and control.

Sensor network platforms and tools:

Lectures: 06

Sensor node hardware, berkeley motes, programming challenges, node-level software platforms, node-level simulators, state-centric programming.

Application and Case studies

Lectures: 04

Text Books:

1. Protocols and Architectures for Wireless Sensor Networks, Holger Karl & Andreas Willig, John Wiley, 2005.
2. Wireless Sensor Networks- An Information Processing Approach, Feng Zhao & Leonidas J. Guibas, Elsevier, 2007.

Reference Books:

1. Wireless Sensor Networks-Technology, Protocols, and Applications, Kazem Sohraby, Daniel Minoli, & Taieb Znati, John Wiley, 2007.
2. Wireless Sensor Network Designs, Anna Hac, John Wiley, 2003.

ECL 1XXX**Numerical Techniques in Electromagnetics****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Understanding FEM, FDTD, MoM methods of computation
- CO2 Application of computation methods to analyze simple microstrip and dipole antennas
- CO3 Application of computation methods to analyze simple cavity resonators
- CO4 Application of computation methods to analyze simple bandpass and bandstop filters

Syllabus**Review EM theory & EM problem:****Lectures: 03**

Review of EM Theory, Classification of EM Problems, boundary condition.

Review of analytical method:**Lectures: 04**

Separation of variable, Laplace's equation and wave equation in different coordinate system, orthogonal functions.

Finite difference method:**Lectures: 06**

Finite difference scheme, differencing of parabolic, hyperbolic and elliptic PDEs, application to practical boundary value problems.

Variational method:**Lectures: 06**

Elements of calculus of variation, construction of functionals from PDEs, Rayleigh methods, weighted residual methods, Galerkin method, and practical application.

Moment methods:**Lectures: 07**

Elements of Integral equation, Green's function, application to quasi-static problem, scattering problems, radiation problems, etc.

Finite element method:**Lectures: 07**

Solution of Laplace's equation, Poisson equation & wave equation, mesh generation in 2D & 3D, FEM for exterior problems.

FDTD:**Lectures: 07**

FDTD analysis in one and two dimensions, the FDTD grid and the Yee algorithm, numerical stability, absorbing boundary conditions and perfectly matched layers.

Text Books:

1. Numerical Techniques in Electromagnetics, M. Sadiku, CRC Press, 3rd edition, 2009.
2. Analytical and Computational methods in Electromagnetics, Ramesh Garg, Artech house, 1st edition, 2008.

ECL 1XXX

Advanced Semiconductor Device Physics

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Understand application and process of carrier transport, tunneling and high field effects
- CO2 Detailed understanding of semiconductor junctions, metal-semiconductor junction and heterojunction devices
- CO3 Understand the fundamentals of non-ideal effects in MOSFETs and MESFETs
- CO4 Design advanced FET devices and their fundamental applications

Syllabus

Physics and Properties of Semiconductors:

Lectures: 08

Carrier transport phenomenon, Generation and Recombination of carriers, continuity equation, tunnelling and high field effects.

Physics of Junction Devices:

Lectures: 11

Thermal Equilibrium Condition, Depletion Region, Depletion and Diffusion Capacitances, Current-Voltage Characteristics, Tunnel diode, non-ideal ohmic contacts, Heterojunction, 2D electron gas, GaAs, InP.

Physics of MOSFET, MESFET:

Lectures: 12

MOSFET nonuniform doping and buried-channels, ballistic transport, short-narrow channel effects, radiation and hot-electron effects, SOI MOSFET, Si-SiGe MOSFET, non-ideal MESFET, HEMT.

Advanced FET Devices:

Lectures: 08

DG MOSFET, FinFET, GAA FET, NW MOSFET, TFET, Spin FET

Text Books:

1. Introduction to Semiconductor materials and devices by M. S. Tyagi, John Wiley & Sons, 2008.
2. D.A. Neamen, Semiconductor Physics & Devices, TMH, 2003.
3. RF Pierrett, Semiconductor Device Fundamentals, Pearson, 2006.

Reference Books:

1. C. Kittel, Introduction to solid state physics, Wiley, New York, 1976.
2. G.W. Hanson, Fundamentals of Nanoelectronics, Pearson, 2009.
3. Semiconductor Opto Electronic Devices, Pallab Bhattacharya, 2nd Edition, Prentice Hall of India Pvt., Ltd, 2004.
4. Opto Electronics – As Introduction to materials and devices, Jasprit Singh, McGraw-Hill International Edition, 1996.
5. B. G. Streetman and Sanjay Banerjee, Solid State Electronic Devices, Pearson, 2008.
6. S. M. Sze, Physics of Semiconductor Devices, Wiley, New York, 1981.

ECL 1XXX**Semiconductor Process Technology****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Overview of basic CMOS process and fabrication
- CO2 Understanding of semiconductor fabrication and crystal growth
- CO3 Understand basics of front-end and back-end processes
- CO4 CMOS integration and packaging of devices to be understood

Syllabus**Introduction:****Lectures: 03**

Integrated Circuits and Planar Process, IC Families, CMOS Process flow.

Fabrication Laboratory and Crystal Growth for VLSI Technology:**Lectures: 07**

Environment, Semiconductor technology trend, Clean rooms, Wafer cleaning, Semiconductor Substrate Phase diagram and solid solubility, Crystal structure, Crystal defects, Czochralski growth, Bridgman growth of GaAs, Float Zone growth, Wafer Preparation and specifications.

Front end process units:**Lectures: 10**

Cleaning, Etching, Lithography, Oxidation, Diffusion, Implantation, Activation, Step Coverage

Back-end process units:**Lectures: 08**

Contacts, Interconnects, Vias, Silicide Gates and Source/Drain Regions, IMD Deposition and Planarization, Chemical-Mechanical Polishing, Electromigration.

CMOS Process Integration**Lectures: 04****Measurements and Packaging:****Lectures: 05**

Conductivity type, Resistivity, Hall Effect Measurements, Drift Mobility; Integrated circuit packages, Electronics package reliability; Technology trends affecting testing.

Text Books:

1. Fundamentals of Semiconductor Fabrication, G. S. May and S. M. Sze, Wiley India, 2004.
2. VLSI Technology, S. M. Sze, 2nd edition, TMH, 2004.
3. Semiconductor Devices: Physics and Technology, S. M. Sze, 2nd Edn., Wiley India, 2011.

Reference Books:

1. Silicon VLSI Technology, Fundamentals, J. D. Plummer, M. D. Deal and P. B. Griffin, Practice and Modeling, Pearson education, 2000.
2. Semiconductor Integrated Circuit Processing Technology, W. R. Runyan and K. E. Bean, Addison Wesley Publishing Company, 1990.
3. The Science and Engineering of Microelectronic Fabrication S. A. Campbell, Oxford University Press, 1996.
4. Fundamentals of Microfabrication, M. J. Madou, 2nd Edition, CRC Press, 2011.

ECL 1XXX**VHDL Modelling****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Basic overview of VHDL (design units, data objects, signal drivers etc)
- CO2 Understand the subprograms (functions, procedures etc)
- CO3 Understand the combinational logic circuit design
- CO4 Understand the asynchronous and synchronous sequential circuits design
- CO5 Basics of placement, routing and architecture (CPLD and FPGA)

Syllabus**Introduction:****Lectures: 05**

Introduction to VHDL, design units, data objects, signal drivers, inertial and transport delays, delta delay, VHDL data types, concurrent and sequential statements.

Subprograms:**Lectures: 05**

Functions, Procedures, attributes, generio, generate, package, IEEE standard logic library, file I/O, test bench, component declaration, instantiation, configuration.

Combinational logic circuit design and VHDL implementation of following circuits: Lectures: 08

Fast adder, Subtractor, decoder, encoder, multiplexer, ALU, barrel shifter, 4X4 key board encoder, multiplier, divider, Hamming code encoder and correction circuits.

Synchronous sequential circuits design:**Lectures: 08**

Finite state machines, Mealy and Moore, state assignments, design and VHDL implementation of FSMs, Linear feedback shift register (Pseudorandom and CRC).

Asynchronous sequential circuit design:**Lectures: 08**

Primitive flow table, concept of race, critical race and hazards, design issues like metastability, synchronizers, clock skew and timing considerations.

Placement, Routing and Architecture:**Lectures: 06**

Introduction to place & route process, Introduction to ROM, PLA, PAL, Architecture of CPLD and FPGA. Digital system design using FPGA.

Text Books:

1. Fundamentals of Digital Logic with VHDL design, Stephen Brown, Zvonko Vranesic, 3e, TMH, 2008.
2. VHDL, Douglas Perry, TMH, 3rd edition, 1998.
3. Digital Design Principles, Fletcher.
4. VHDL Synthesis, J Bhasker.
5. VHDL Primer, J Bhasker, Pearson Education.

Reference Books:

1. Digital System Design Using VHDL, Chales H. Roth.
2. Digital System Design, John Wakerley.
3. VHDL, Zainalabedin Navabbi.
4. VHDL, D. Smith.

ECL 1XXX**Detection and Estimation Theory****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Understand the basics of detection and estimation theory
- CO2 To understand and develop the ability to design, automated systems for detection and estimation
- CO3 Examine the detection of deterministic and random signals using statistical models
- CO4 Understand various estimation schemes such as ML and MMSE estimators
- CO5 Analyzing signal estimation in time domain using filters

Syllabus**Background material:****Lectures: 02**

Recap of probability, calculus, and linear algebra.

Estimation theory:**Lectures: 04**

Minimum variance unbiased estimation, best linear unbiased estimation, cramer-rao lower bound (CRLB).

Maximum likelihood estimation (MLE):**Lectures: 04**

Exact and approximate methods: EM, alternating max, etc.

Bayesian inference & least squares estimation:**Lectures: 06**

Basic ideas, adaptive techniques, Recursive LS, etc. kalman filtering (sequential Bayes).

Finite state hidden markov models:**Lectures: 06**

Forward-backward algorithm, viterbi (ML state estimation), parameter estimation (f-b + EM), and graphical models.

Applications:**Lectures: 06**

Image processing, speech, communications. Sparse recovery and compressive sensing introduction.

Monte-Carlo methods:**Lectures: 06**

Importance sampling, MCMC, particle filtering, applications in numerical integration (MMSE estimation or error probability computation) and in numerical optimization (e.g., annealing).

Detection theory:**Lectures: 06**

Likelihood ratio testing, bayes detectors, minimax detectors, multiple hypothesis tests, neyman pearson detectors (matched filter, estimator correlator etc), wald sequential test, generalized likelihood ratio tests (GLRTs), wald and RAO scoring tests, applications.

Text Books:

1. Detection, Estimation, and Modulation Theory, H.VanTrees.
2. Fundamentals of Statistical Signal Processing Voll: Estimation Theory, S.M. Kay.
3. Fundamentals of Statistical Signal Processing VolIII: Detection Theory, S.M. Kay.

Reference Books:

1. Linear Estimation, Kailath, Sayed and Hassibi.
2. An Introduction to Signal Detection and Estimation, V. Poor.
3. Monte Carlo Strategies in Scientific Computing, J.S. Liu, Springer-Verlag, 2001.
4. Stochastic Simulation, B.D. Ripley, Wiley, 1987.

Open Elective(s)

ECL 1XXX

Nano Electronics

L-T-P: 3-0-0

Course Outcome

Credits: 3

CO1 Introduction of the basics of quantum theory and its applications in Electronics

CO2 Understanding of CMOS scaling and development of novel nanodevices

CO3 Understand the fundamentals of nanodevice using molecular theory

Syllabus

Principles of Quantum Mechanics:

Lectures: 10

Wave nature of particles and wave-particle duality, Pauli-exclusion principle, wave functions and Schrodinger's equations. Transport mechanisms: drift, diffusion, and Ballistic. Quantum dots, wires, and wells. Principles of optical devices, geometry as the material parameter

Scaling approaches:

Lectures: 15

CMOS scaling: advantages and limitations. Nanoscale MOSFETs, FINFETs, Vertical MOSFETs, system integration limits (interconnect issues etc.), Tunneling Transistors, Single electron transistors, spintronics, and Junction-less Transistor.

Bottom-up approaches:

Lectures: 15

Single molecules as electronic devices, transport in molecular structures, molecular systems as alternatives to conventional electronics, molecular interconnects; Carbon nanotube electronics, band structure, applications.

Text Books:

1. Solid State Physics, Ashcroft and Mermin, Thomson Press (India) Ltd, 2003
2. Introduction to Nanotechnology, C.P. Poole Jr., F. J. Owens, Wiley, 2003.
3. Fundamentals of Nanoelectronics, G. W. Hanson, Pearson, 2007.

Reference Books:

1. Nanosystems: molecular, machinery, manufacturing and computation, K.E. Drexler, Wiley, 1992.
2. The Physics of Low-Dimensional Semiconductors, John H. Davies, Cambridge University Press, 1998.
3. Nanoelectronics and Information Technology (Advanced Electronic Materials and Novel Devices), Waser Ranier, Wiley-VCH, 3e, 2012.
4. Introduction to Solid State Physics, C. Kittel, Wiley, 2012

ECL 1XXX

Wireless Sensors Networks

L-T-P: 3-0-0

Course Outcome

Credits: 3

- CO1 Identify the components of Wireless Sensor Networks
- CO2 Understand the challenges in network coverage and routing for energy efficiency
- CO3 Define node Architecture for specific applications
- CO4 Program sensor network platforms using specialized operating system
- CO5 Recognize upcoming challenges in Sensor Networks.

Syllabus

Overview of wireless sensor networks:

Lectures: 02

Introduction to Adhoc networks, mobile adhoc networks and sensor networks. Challenges for wireless sensor networks, enabling technologies for wireless sensor networks.

Architectures:

Lectures: 08

Single node architecture, hardware components, energy consumption of sensor nodes, operating systems and execution environments, network architecture, sensor network scenarios, optimization goals and figures of merit, gateway concepts

Networking sensors:

Lectures: 10

Physical layer and transceiver design considerations, MAC protocols for wireless sensor networks, low duty cycle protocols and wakeup concepts, S-MAC, the mediation device protocol, wakeup radio concepts, address and name management, assignment of MAC addresses, routing protocols, energy, efficient routing, geographic routing.

Infrastructure establishment:

Lectures: 10

Energy efficient topology, Clustering- LEACH, PEGASIS, ELBERA etc. Time synchronization, localization and positioning, sensor tasking and control.

Sensor network platforms and tools:

Lectures: 06

Sensor node hardware, berkeley motes, programming challenges, node-level software platforms, node-level simulators, state-centric programming.

Application and Case studies

Lectures: 04

Text Books:

1. Protocols and Architectures for Wireless Sensor Networks, Holger Karl & Andreas Willig, John Wiley, 2005.
2. Wireless Sensor Networks- An Information Processing Approach, Feng Zhao & Leonidas J. Guibas, Elsevier, 2007.

Reference Books:

1. Wireless Sensor Networks-Technology, Protocols, and Applications, Kazem Sohraby, Daniel Minoli, & Taieb Znati, John Wiley, 2007.
2. Wireless Sensor Network Designs, Anna Hac, John Wiley, 2003.

ECL 1XXX**VHDL Modelling****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Basic overview of VHDL (design units, data objects, signal drivers etc)
- CO2 Understand the subprograms (functions, procedures etc)
- CO3 Understand the combinational logic circuit design
- CO4 Understand the asynchronous and synchronous sequential circuits design
- CO5 Basics of placement, routing and architecture (CPLD and FPGA)

Syllabus**Introduction:****Lectures: 05**

Introduction to VHDL, design units, data objects, signal drivers, inertial and transport delays, delta delay, VHDL data types, concurrent and sequential statements.

Subprograms:**Lectures: 05**

Functions, Procedures, attributes, generio, generate, package, IEEE standard logic library, file I/O, test bench, component declaration, instantiation, configuration.

Combinational logic circuit design and VHDL implementation of following circuits: Lectures: 08

Fast adder, Subtractor, decoder, encoder, multiplexer, ALU, barrel shifter, 4X4 key board encoder, multiplier, divider, Hamming code encoder and correction circuits.

Synchronous sequential circuits design:**Lectures: 08**

Finite state machines, Mealy and Moore, state assignments, design and VHDL implementation of FSMs, Linear feedback shift register (Pseudorandom and CRC).

Asynchronous sequential circuit design:**Lectures: 08**

Primitive flow table, concept of race, critical race and hazards, design issues like metastability, synchronizers, clock skew and timing considerations.

Placement, Routing and Architecture:**Lectures: 06**

Introduction to place & route process, Introduction to ROM, PLA, PAL, Architecture of CPLD and FPGA. Digital system design using FPGA.

Text Books:

1. Fundamentals of Digital Logic with VHDL design, Stephen Brown, Zvonko Vranesic, 3e, TMH, 2008.
2. VHDL, Douglas Perry, TMH, 3rd edition, 1998.
3. Digital Design Principles, Fletcher.
4. VHDL Synthesis, J Bhasker.
5. VHDL Primer, J Bhasker, Pearson Education.

Reference Books:

1. Digital System Design Using VHDL, Chales H. Roth.
2. Digital System Design, John Wakerley.
3. VHDL, Zainalabedin Navabbi.
4. VHDL, D. Smith.

ECL 1XXX**CAD for VLSI****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Distinguish between DSP and FPGA based filter architectures
- CO2 Compare the architectures of general-purpose processors and DSP processors
- CO3 Design simple IP cores for FPGA applications
- CO4 Use the CAD tools to model an FPGA design
- CO5 Model and design a heterogeneous FPGA based embedded system

Syllabus**Introduction:****Lectures: 04**

VLSI design flow, Full-custom, standard-cell, gate-array and FPGA, VLSI Design automation tools.

Basic concepts of high-level synthesis:**Lectures: 04**

Partitioning, scheduling, allocation and binding.

Verilog / VHDL:**Lectures: 10**

Introduction and use in synthesis, modeling combinational and sequential logic, Procedures, assignments and control statements in Verilog, writing test benches.

Technology mapping and Test-ability issues:**Lectures: 08**

Fault modeling and simulation, test generation, design for test-ability, built-in self-test, Testing SoC's, Basic concepts of verification, Silicon Compiler.

Algorithmic Graph Theory:**Lectures: 04**

Data structure for graph representation, graph algorithms – depth first search, breadth first search, Dijkstra's shortest path, Prim's algorithm.

Physical design automation algorithms:**Lectures: 10**

Floor-planning, placement, routing, layout synthesis, design rule check, compaction, power and delay estimation, clock and power routing, etc. Special considerations for analog and mixed-signal designs.

Text Books:

1. VHDL: Programming by Example, Douglas Perry, McGraw-Hill Professional; 4 ed., 2002.
2. Verilog HDL, Samir Palnitkar, 2 ed, Pearson, 2004
3. Algorithms for VLSI physical design automation, N. A. Sherwani, Kluwer Academic Publishers, 1999.

Reference Books:

1. Algorithms for VLSI Design Automation, S. H. Gerez, Wiley-India, 1999.
2. Synthesis and Optimization of Digital Circuits, Giovanni De Micheli, Tata McGraw Hill, 1994.
3. VLSI physical design automation: theory and practice, S. M. Sait and H. Youssef, World Scientific Pub. Co., 1999.
4. RTL Hardware Design using VHDL: Coding for efficiency, Portability, and Scalability, Pong P. Chu, John Wiley- & Sons Inc., Hoboken, New Jersey, 2006.
5. An Introduction to Physical Design, M. Sarrafzadeh and C. K. Wong, McGraw Hill, 1996.

ECL 1XXX**Information Theory and Coding****L-T-P: 3-0-0****Course Outcome****Credits: 3**

- CO1 Understand the basic concepts of information theory, mutual information and characteristics of various types of communication noisy channels
- CO2 Discuss the concepts of entropy, channel capacity and various source coding schemes
- CO3 Discuss the various channel coding techniques for error-free transmission of message over a noisy channel
- CO4 Understand the basic techniques of protecting information from unauthorized access and concepts of cryptography

Syllabus**Introduction:****Lectures: 25**

Entropy and mutual information for discrete ensembles, source coding, variable length coding, discrete memoryless channels, mutual information, channel capacity, channel coding theorem, differential entropy and mutual information for continuous ensembles, channel capacity for Gaussian channels, channel coding, linear block codes, and cyclic codes, convolution codes, sequential and probabilistic decoding, majority logic decoding, burst error-correcting codes, turbo codes and low-density-parity check codes.

Cryptography:**Lectures: 15**

Basic concepts on cryptography and crypto analysis, security issues; private-key encryption algorithms-stream ciphers, block ciphers, Shannon's theory; introduction to number theory – modular arithmetic, exponentiation and discrete logarithms in Galois field; public key encryption algorithms-Diffie-Hellman public-key distribution scheme, RSA public-key cryptosystem; Message authentication, hashing functions, digital signatures.

Text Books:

1. Communication Systems, S. Haykin, 4th Edition, John Wiley & Sons, New York, 2001.
2. Elements of Information Theory, T M Cover and J A Thomas, John Wiley, 1991.
3. Information Theory, Coding and Cryptography, R. Bose, Tata McGraw-Hill, 2002.

Reference Books:

1. Introduction to Cryptography with Coding Theory, Wade Trappe, Lawrence C. Washington, 2nd Edition, Pearson Education India, 2007.
2. Modern digital and Analog communications, B. P. Lathi, 3rd Edition, Oxford University Press, 2000.
3. Cryptography: Theory and Practice, D. R. Stinson, 3rd Edition, Champmen & Hall/CRC, 2006.